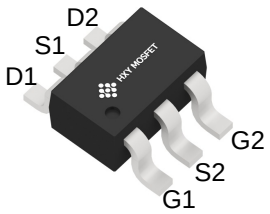
BSL215CH6327

Dual N+P-Channel Enhancement Mode MOSFET

Description

The BSL215CH6327 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.



SOT-23-6L
(TSOP-6-1.5mm)

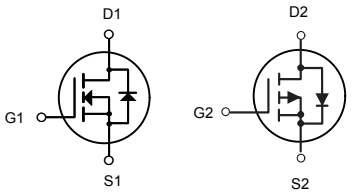
General Features

V_{DS} = 20V I_D =5A

$R_{DS(ON)}$ < 27mΩ @ V_{GS}=4.5V

V_{DS} = -20V I_D =-3.6A

$R_{DS(ON)}$ < 70 mΩ @ V_{GS}=-4.5V



N-Channel MOSFET P-Channel MOSFET

Application

Wireless charging

Boost driver

Brushless motor

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
BSL215CH6327	SOT-23-6L(TSOP-6-1.5mm)	HXY MOSFET	3000

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
VDS	Drain-Source Voltage	20	-20	V
VGS	Gate-Source Voltage	±12	±12	V
I_D@T_A=25℃	Continuous Drain Current, V_{GS} @ 10V[1]	5	-3.6	A
I_D@T_A=70℃	Continuous Drain Current, V_{GS} @ 10V[1]	4	-2.5	A
IDM	Pulsed Drain Current[2]	20	-12	A
EAS	Single Pulse Avalanche Energy[3]	72	59	mJ
IAS	Avalanche Current	21	-19	A
P_D@T_A=25℃	Total Power Dissipation[4]	2.5	2.08	W
TSTG	Storage Temperature Range	-55 to 150	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	℃
$R_{\theta}JA$	Thermal Resistance Junction-Ambient [1]	85		℃/W
$R_{\theta}JC$	Thermal Resistance Junction-Case[1]	50		℃/W

N-Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=20V, V_{GS}=0V,	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS}=0V, V_{GS} =±12V	-	-	±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250μA	0.4	0.7	1	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note2	V_{GS}=4.5V, I_D=4A	-	22	27	mΩ
		V_{GS}=2.5V, I_D=3A	-	29	44	
C_{iss}	Input Capacitance	V_{DS}=10V, V_{GS}=0V, f=1.0MHz	-	358	-	pF
C_{oss}	Output Capacitance		-	69.3	-	pF
C_{rss}	Reverse Transfer Capacitance		-	58.5	-	pF
Q_g	Total Gate Charge	V_{DS}=10V, I_D=2A, V_{GS}=4.5V	-	5.6	-	nC
Q_{gs}	Gate-Source Charge		-	0.8	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	1	-	nC
$t_{d(on)}$	Turn-on Delay Time	V_{DS}=10V, I_D=4A, R_{GEN}=3Ω, V_{GS}=4.5V	-	5	-	ns
t_r	Turn-on Rise Time		-	30	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	48	-	ns
t_f	Turn-off Fall Time		-	36	-	ns
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	5	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	16	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS}=0V, I_S=4A	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

P-Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D= -250μA	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}= -20V, V_{GS}=0V,	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS}=0V, V_{GS}= ±12V	-	-	±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D= -250μA	-0.5	-0.7	-1.0	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note2	V_{GS}= -4.5V, I_D= -3A	-	55	70	mΩ
		V_{GS}= -2.5V, I_D= -2A	-	70	100	
C_{iss}	Input Capacitance	V_{DS}= -10V, V_{GS}=0V, f=1.0MHz	-	503	-	pF
C_{oss}	Output Capacitance		-	67	-	pF
C_{rss}	Reverse Transfer Capacitance		-	58	-	pF
Q_g	Total Gate Charge	V_{DS}= -10V, I_D= -2A, V_{GS}= -4.5V	-	4.1	-	nC
Q_{gs}	Gate-Source Charge		-	0.8	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	1.1	-	nC
$t_{d(on)}$	Turn-on Delay Time	V_{DD}= -10V, I_D= -3A, R_G=1Ω, V_{GEN}= -4.5V, R_L=1.2Ω	-	11	-	ns
t_r	Turn-on Rise Time		-	52	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	16	-	ns
t_f	Turn-off Fall Time		-	10	-	ns
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-3	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-12	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS}=0V, I_S= -3A	-	-	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Performance Characteristics

N-Channel Typical Characteristics

Figure1: Output Characteristics

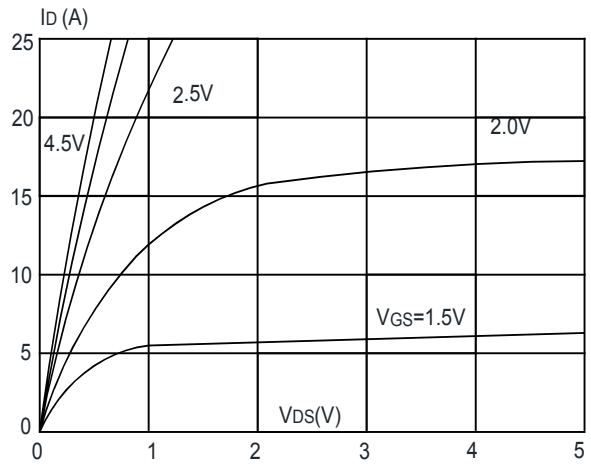


Figure 2: Typical Transfer Characteristics

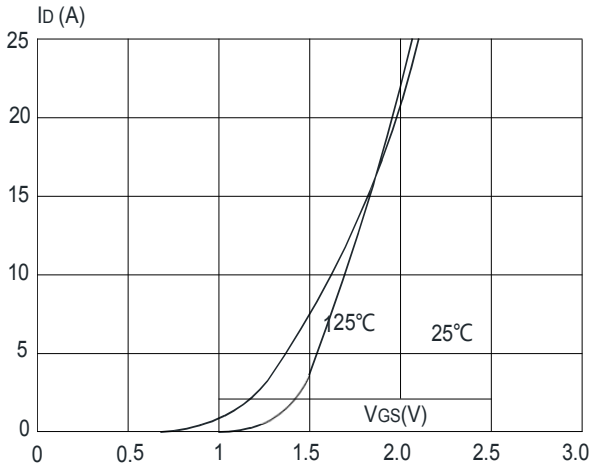


Figure 3: On-resistance vs. Drain Current

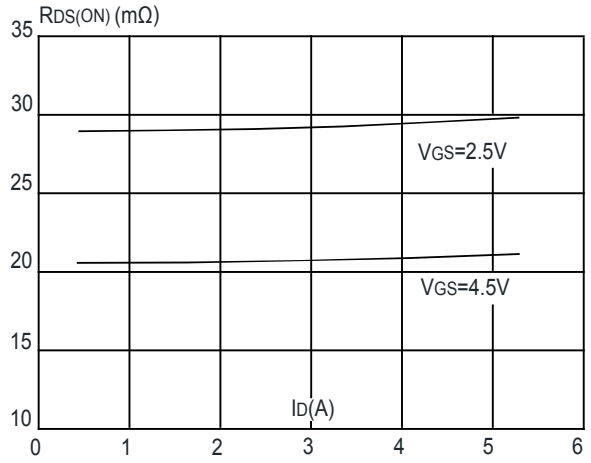


Figure 4: Body Diode Characteristics

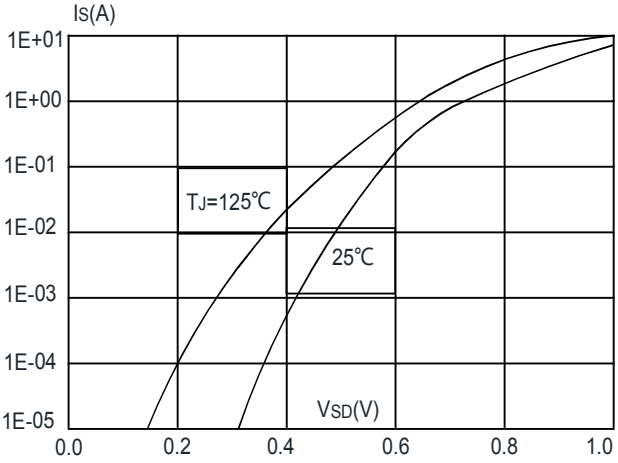


Figure 5: Gate Charge Characteristics

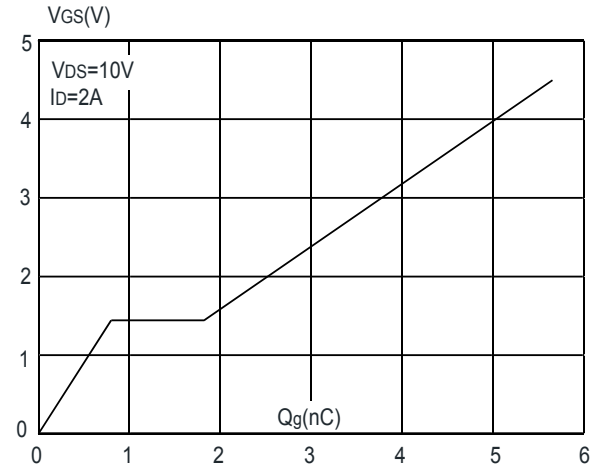
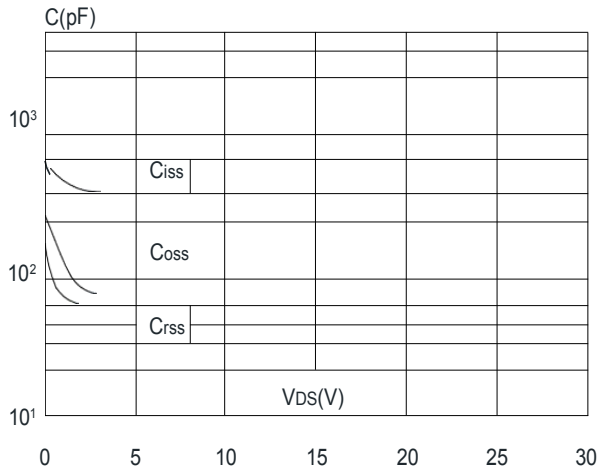


Figure 6: Capacitance Characteristics



P-Channel Typical Characteristics

Figure1: Output Characteristics

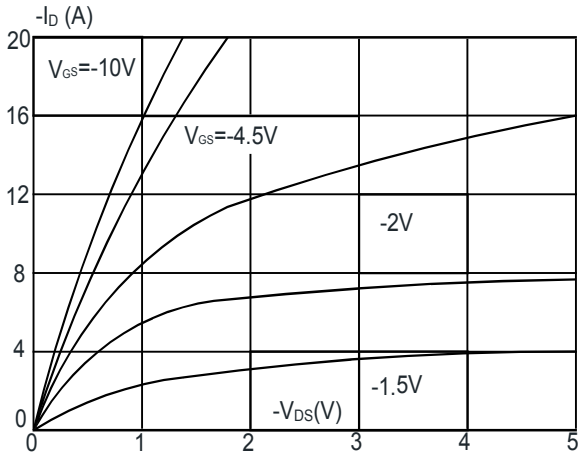


Figure 2: Typical Transfer Characteristics

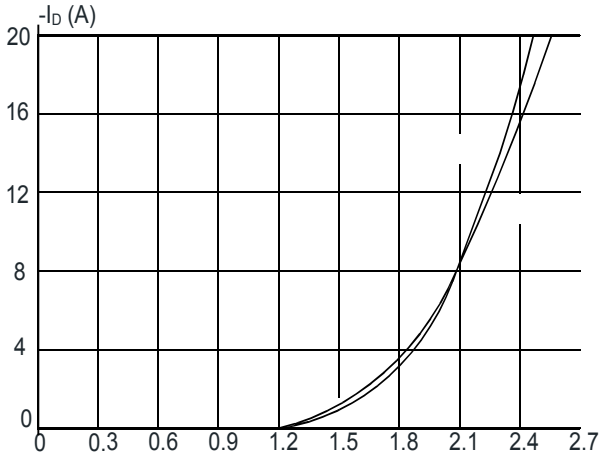


Figure 3: On-resistance vs. Drain Current

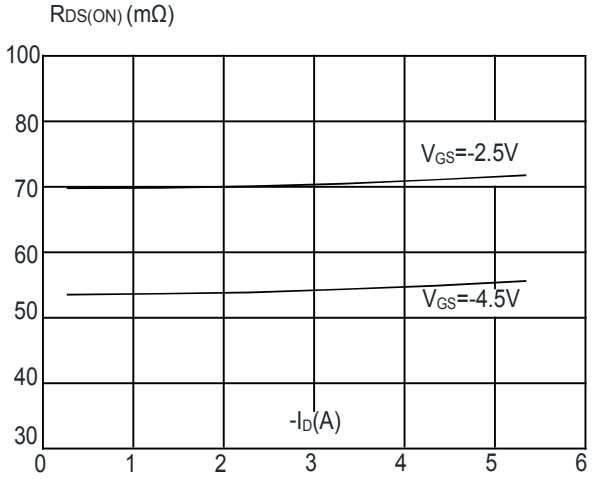


Figure 4: Body Diode Characteristics

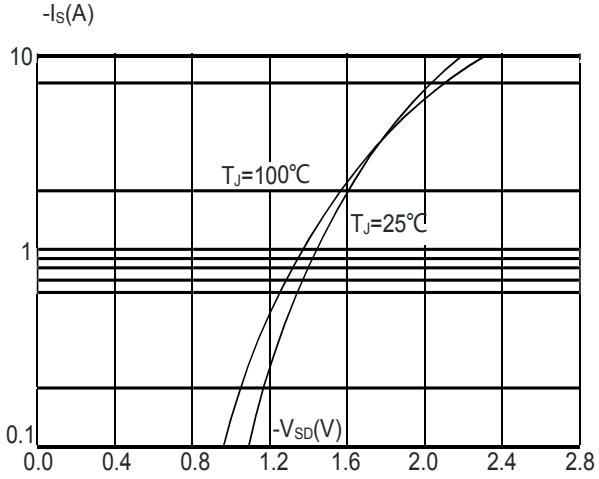


Figure 5: Gate Charge Characteristics

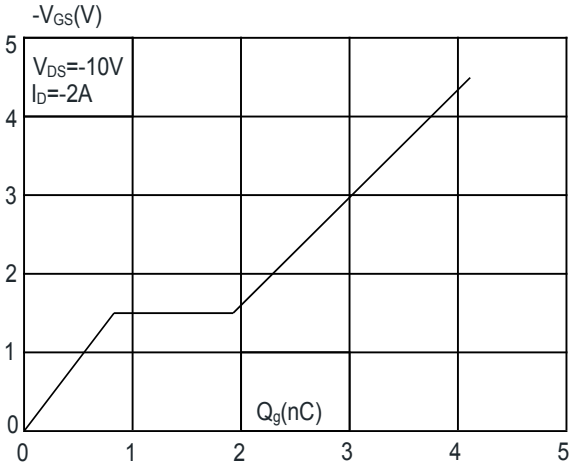
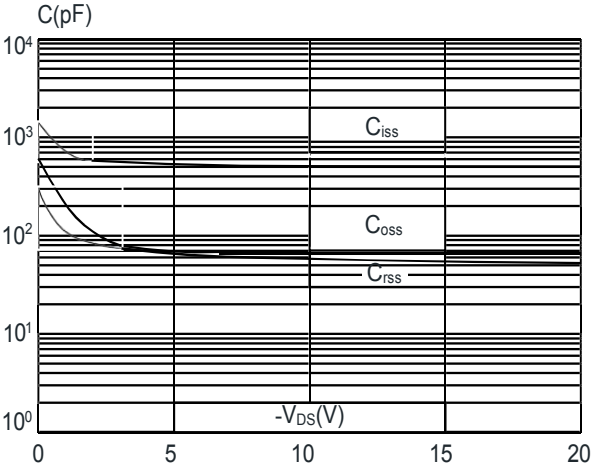
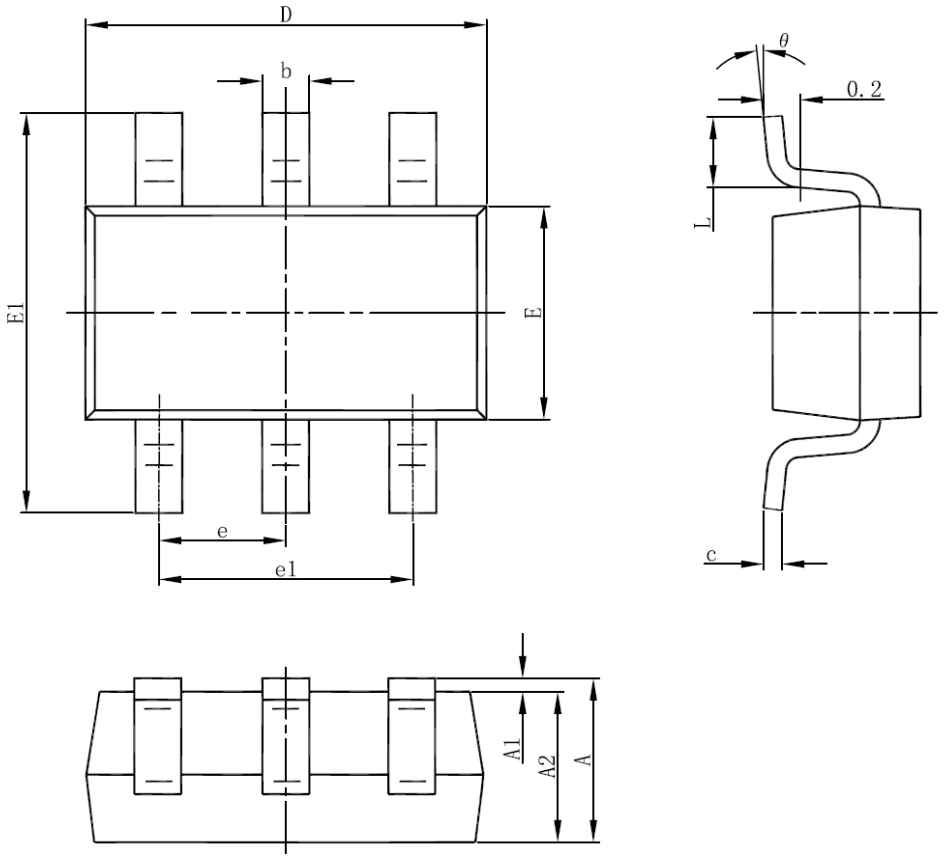


Figure 6: Capacitance Characteristics



SOT-23-6L(TSOP-6-1.5mm) Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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HESDNC5VB1AF-B HT7540-1 HXY2101EI P6SMB33CA SD15C SS510C BZT52C3V3S HC3D10170A
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