TPTLV809 Series

3-Pin Microprocessor Reset Circuits

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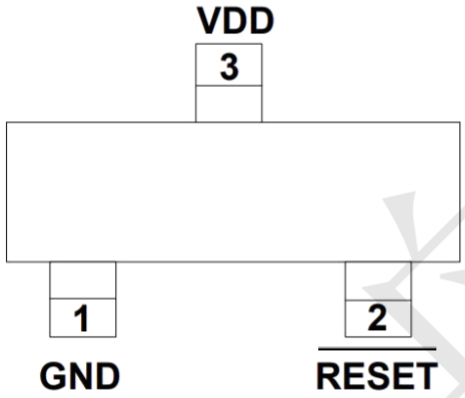
Features

- Typical Quiescent Current of 3μA
- No External Components
- V_{CC} Transient Immunity
- Correct Logic Output Guaranteed to V_{CC}=1.0V
- Push-Pull Low Active Reset Output
- Packages SOT23

Applications

- Computers
- Embedded Systems
- Battery-Powered Equipments
- Intelligent Instruments
- CPU and Logic Circuit Reset
- Power Fail Detectors
- Automotive

Pin Definition



SOT23

Ordering Information

ORDER NUMBER	RESET VOLTAGE
TPTLV809L30DBZR-TP	2.63V
TPTLV809K33DBZR-TP	2.93V

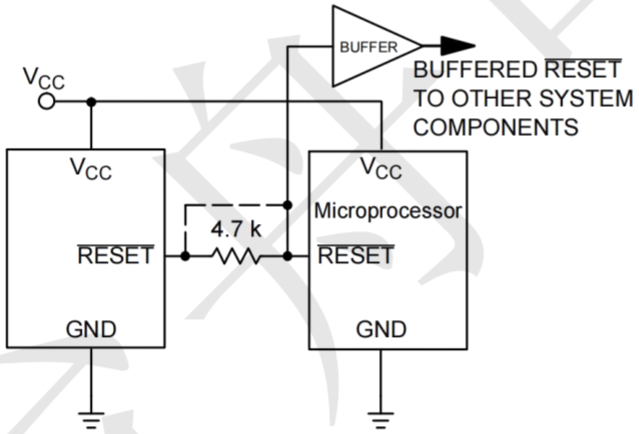
PIN CONFIGURATION

PIN	NAME	FUNCTION
1	GND	Ground
2	$\overline{\text{RESET}}$	$\overline{\text{RESET}}$ output remains low while VCC is below the reset voltage threshold, and for a reset timeout period after VCC rises above reset threshold
3	VCC	Supply Voltage .

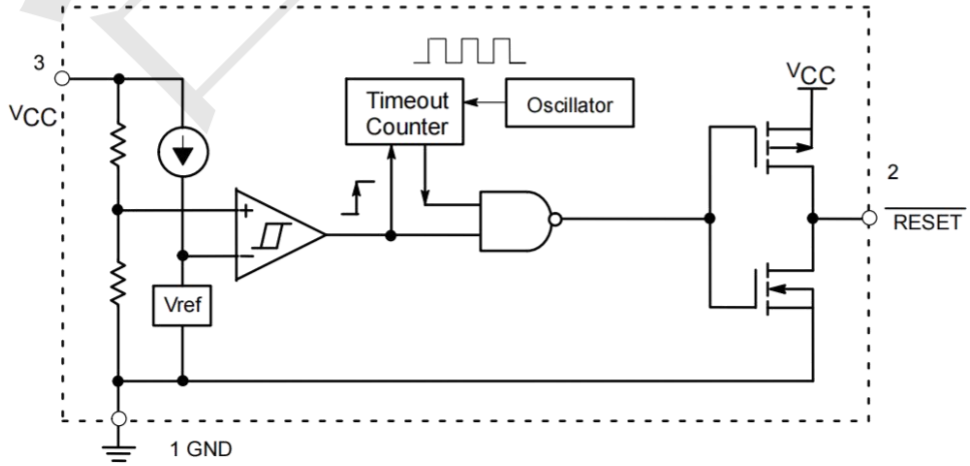
Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Value	UNIT
VCC	Any pin with respect to ground	-0.3 to +6.0	V
ICC	Input Current, VCC	20	mA
IO	Output Current,$\overline{\text{RESET}}$	20	mA
	Rate of Rise, VCC	100	V/μs
PD	Continuous Power Dissipation	250	mW
TA	Operating Temperature Range	-40 to +85	℃
TSTG	Storage Temperature Range	-65 to +105	℃
$R_{\theta JC}$	Thermal Resistance from Junction to Case	110	℃/W
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient	250	℃/W

TYPICAL APPLICATIN CIRCUIT



FUNCTIONAL BLOCK DIAGRAM



Series Complementary Active−Low Output

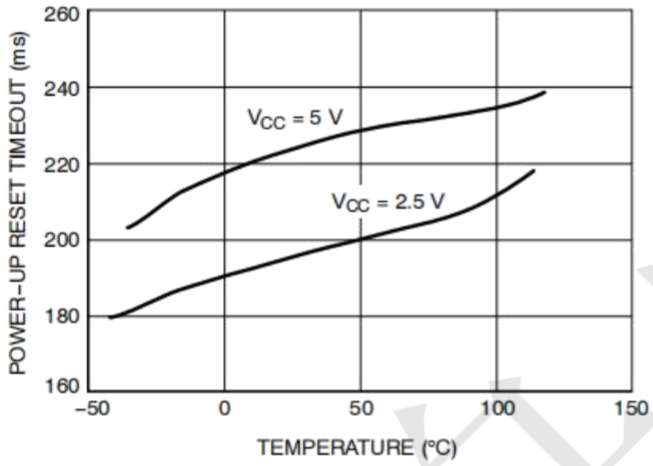
Electrical Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
	V_{CC} Range	T_A=0°C to +70°C		1.0		5.5	V
I_{CC}	Supply Current	V_{CC}<5.5V,			3	5	μA
		V_{CC}<3.6V,			3	5	
V_{TH}	Reset Threshold	K33	T_A=+25°C	2.83	2.93	2.97	V
			T_A=-40°C to +85°C	2.80		3.00	
		L30	T_A=+25°C	2.54	2.63	2.67	
			T_A=-40°C to +85°C	2.51		2.69	
Reset Threshold Tempco					30		ppm/°C
V_{CC} to Reset Delay			V_{CC}=V_{TH} to (V_{TH}–100mV)		10		μs
Reset Active Timeout Period			T_A=-40°C to +85°C	140	240	360	ms
V_{OL}	RESET Output Voltage Low (Push-Pull Active Low)	V_{CC}=V_{TH} min, I_{SINK}=1.2mA,S/T/Z				0.3	V
		V_{CC}=V_{TH} min, I_{SINK}=3.2mA,J/L/M				0.4	
		V_{CC}≤1.0V, I_{SINK}=50μA				0.3	
V_{OH}	RESET Output Voltage High (Push-Pull Active Low)	V_{CC}>V_{TH} max, I_{SOURCE}=500μA, S/T/Z		0.8V_{CC}			V
		V_{CC}>V_{TH} max, I_{SOURCE}=800μA, J/L/M		V_{CC}–1.5			

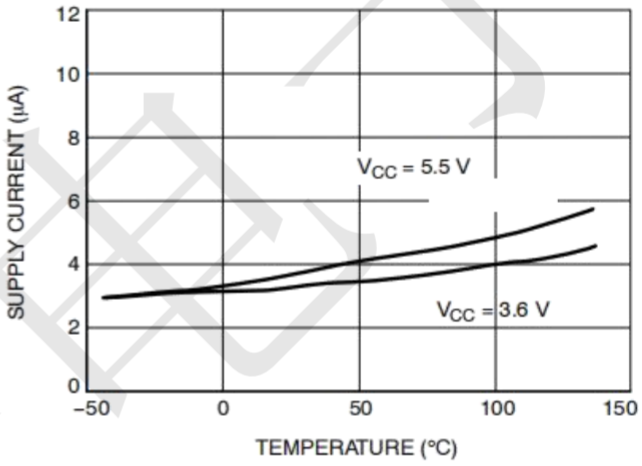
Note 1: Production testing done at TA=+25°C; limits over temperature guaranteed by design only.

Typical Operating Characteristics

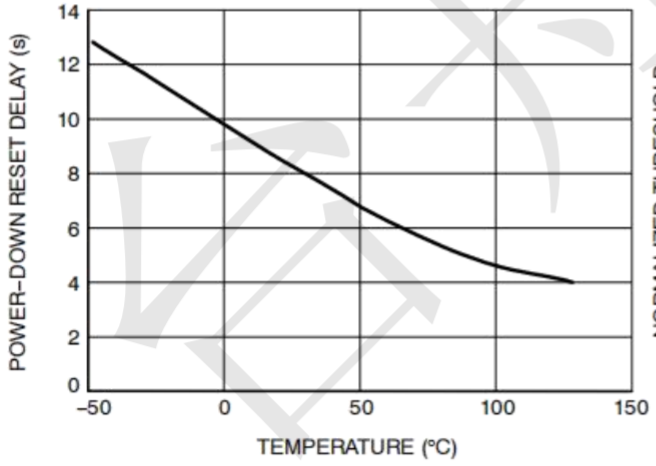
(VCC = Full range, TA = −40°C to +85°C unless otherwise noted. Typical values at TA = +25°C and VCC = 5 V for the L/M/J versions,VCC = 3.3 V for the T/S versions, VCC = 3 V for the R version and VCC = 2.5 V for the Z version.)



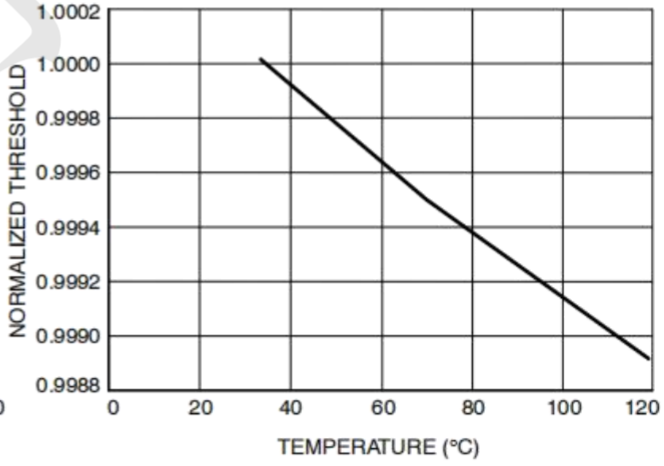
Power−Up Reset Timeout vs. Temperature



Supply Current vs. Temperature (No Load,)



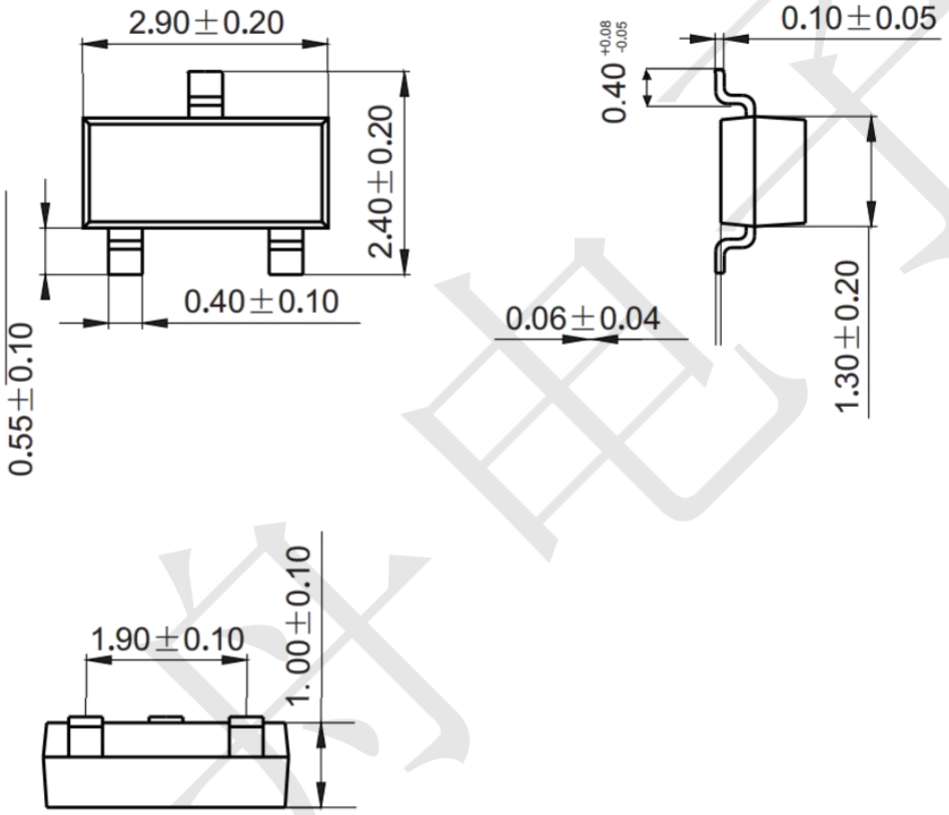
Power−Down Reset Delay vs. Temperature



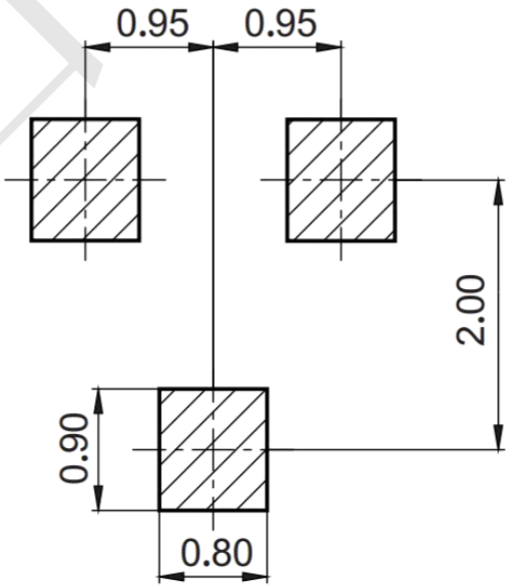
Normalized Reset Threshold vs. Temperature

Package Outline Dimensions (unit: mm)

SOT23



Mounting Pad Layout (unit: mm)



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TECH PUBLIC:

BSN20W DMP6350S TPB05A40-323 TPESD0402G08V BV15C DDTA143ZCA TPS7B6950DBVR-TP ESD5B5.0ST1G-P FDC6301N-TP TPADM809LAKSZ TPB1A30-123 TPD1213A-02SM TPESD0504S6-A SI3932DV SN7002W TPFMMT493 TPGC12C-1 TPZXMP3A13FTA QM3003J-TP TPADM809SAKSZ TPDMG6602SVT TPSPX3819M5-L-3-3 TPZVN3320FTA FDMS7670AS-TP IRFR2405TRPBF-TP LTH7 TPAPX809S-31SA TPWSB5557Z TPESD5311N TPS3809L30DBVR-TP