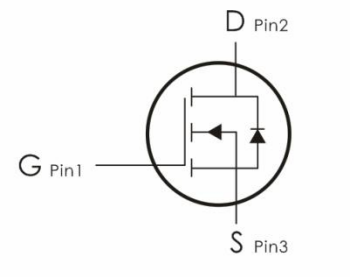
Description：

This N-Channel MOSFET uses advanced SGT technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.





Features：

1) V_{DS}=150V,I_D=25A,$R_{DS(ON)}$<70mΩ @V_{GS}=10V（Typ：64mΩ）
2) Low gate charge.
3) Green device available.
4) Advanced high cell denity SGT technology for ultra low $R_{DS(ON)}$.
5) Excellent package for good heat dissipation.

Package Marking and Ordering Information：

Part NO.	Marking	Package	Packing
PI070TG-B	I070T-B	TO- 220	50 pcs/Tube

Absolute Maximum Ratings：(T_C=25℃ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Continuous Drain Current[1]	25	A
	Continuous Drain Current-T_C=100℃[1]	17.5	
P_D	Power Dissipation	68	W
I_{DM}	Pulsed Drain Current[2]	100	A
E_{AS}	Single pulse avalanche energy[3]	65	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+150	℃

Thermal Characteristics：

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance,Junction to Case	1.84	℃/W

Electrical Characteristics: (T_C=25℃ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Sourtce Breakdown Voltage	V_{GS}=0V,I_D=250μA	150	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{GS}=0V, V_{DS}=150V	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V, V_{DS}=0A	---	---	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate-Source Threshold Voltage	V_{GS}=V_{DS}, I_D=250μA	2	3	4	V
$R_{DS(ON)}$	Drain-Source On Resistance[4]	V_{GS}=10V,I_D=10A	---	64	70	mΩ
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}=75V, V_{GS}=0V, f=1MHz	---	400	---	pF
C_{oss}	Output Capacitance		---	78.4	---	
C_{rss}	Reverse Transfer Capacitance		---	10.8	---	
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DS}=75V, R_L=7.5A, R_{ENG}=3Ω ,V_{GS}=10V	---	9.5	---	ns
t_r	Rise Time		---	5.5	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	12.5	---	ns
t_f	Fall Time		---	3	---	ns
Q_g	Total Gate Charge	V_{GS}=10V, V_{DS}=75V, I_D=10A	---	5.5	---	nC
Q_{gs}	Gate-Source Charge		---	2.8	---	nC
Q_{gd}	Gate-Drain “Miller” Charge		---	1.8	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Diode Forward Voltage	V_{GS}=0V, I_{SD}=10A	---	---	1.2	V
I_S	Continuous Drain Curren	V_D=V_G=0V	---	---	20.8	A
I_{SM}	Pulsed Drain Current		---	---	83.3	A
Trr	Reverse Recovery Time	I_F=25A, dI/dt=100A/us	---	29	---	ns
Qrr	Reverse Recovery Charge		---	130	---	nC

Notes:

1. Computed continuous current assumes the condition of $T_{j,Max}$ while the actual continuous current depends on the thermal & electro-mechanical application board design
2. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
3. EAS condition : TJ=25℃,VDD=75V,V_G=10V,L=0.5mH
4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Typical Characteristics: (T_C=25℃ unless otherwise noted)

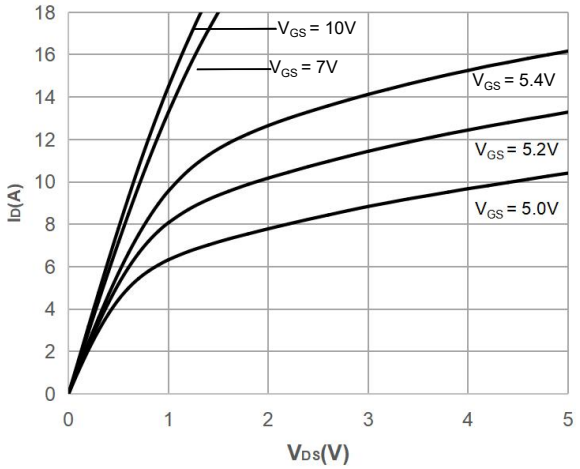


Figure 1: Output Characteristics

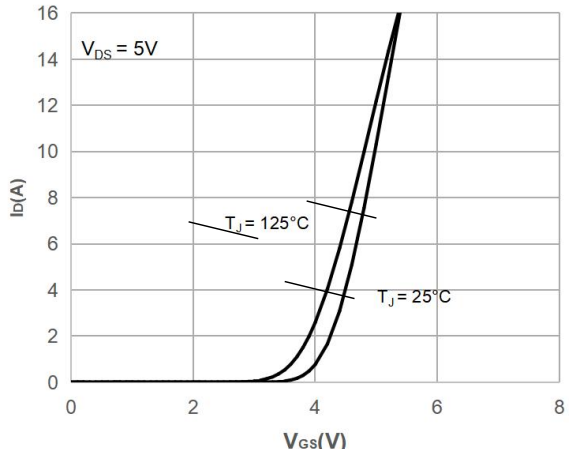


Figure 2: Typical Transfer Characteristics

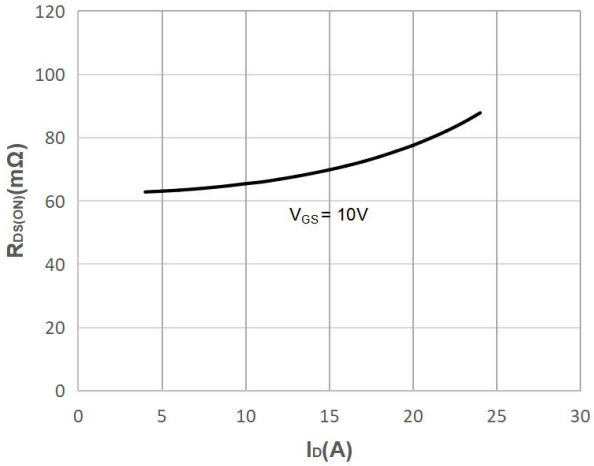


Figure 3: On-resistance vs. Drain Current

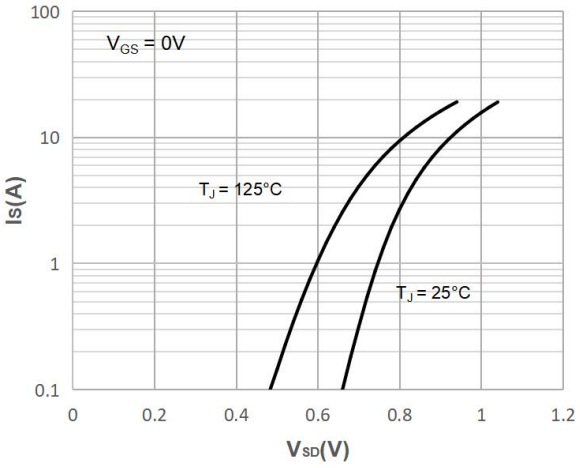


Figure 4: Body Diode Characteristics

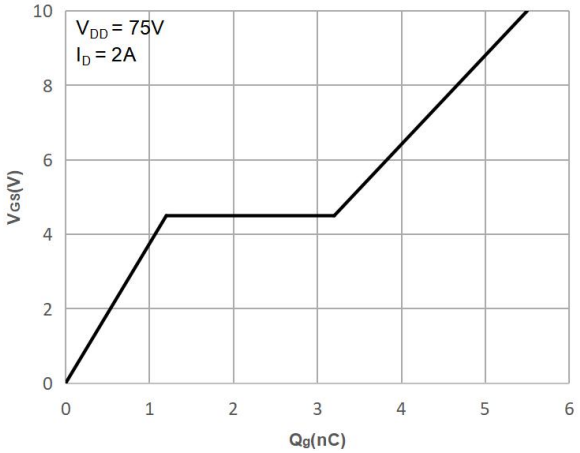


Figure 5: Gate Charge Characteristics

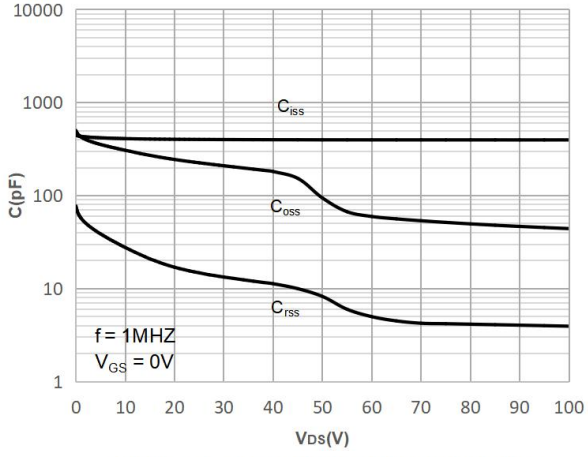


Figure 6: Capacitance Characteristics

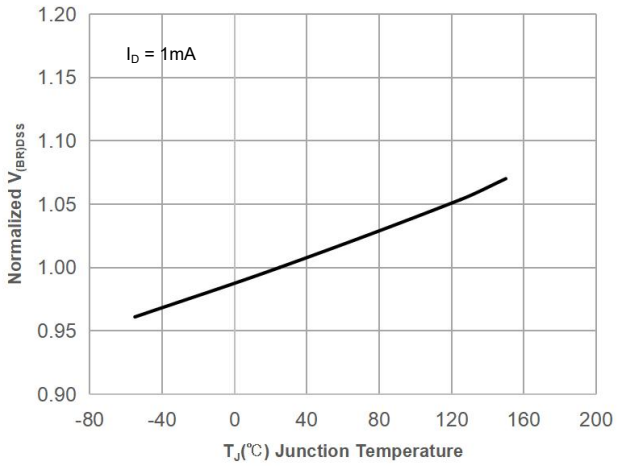


Figure 7: Normalized Breakdown voltage vs. Junction Temperature

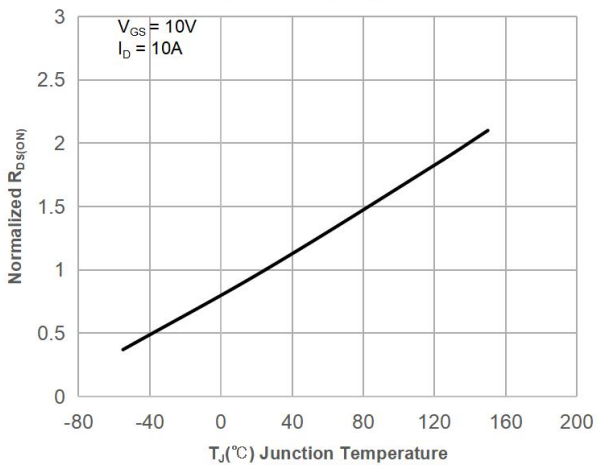


Figure 8: Normalized on Resistance vs.

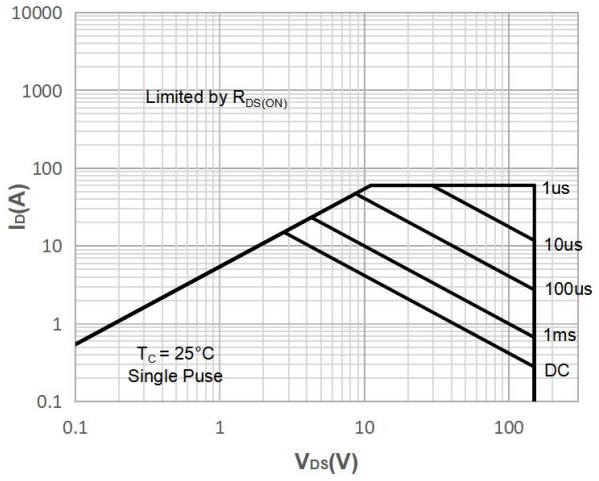


Figure 9: Maximum Safe Operating Area

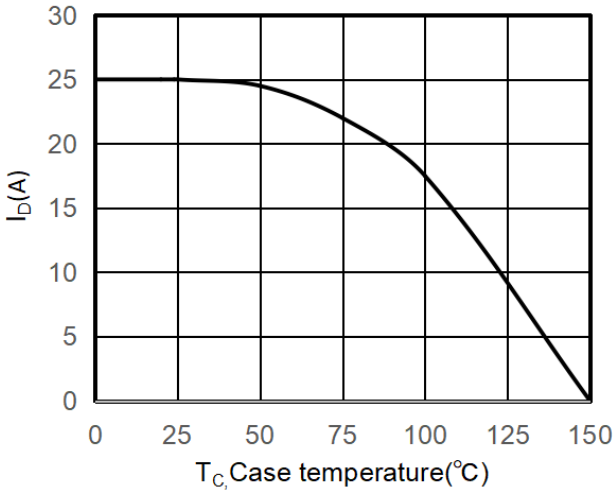


Figure 10: Maximum Continuous Drian Current vs. Case Temperature

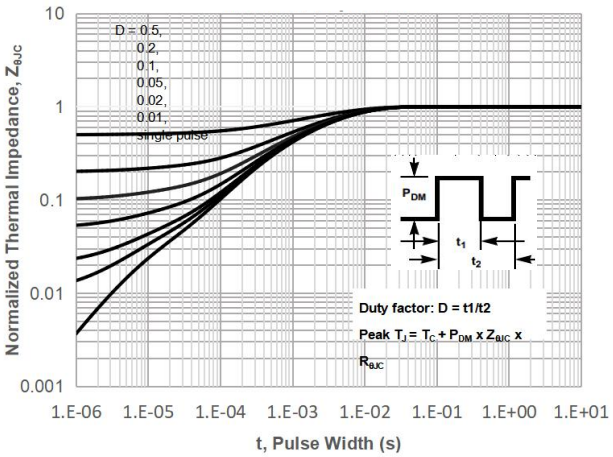


Figure 11: Normalized Maximum Transient Thermal Impedance

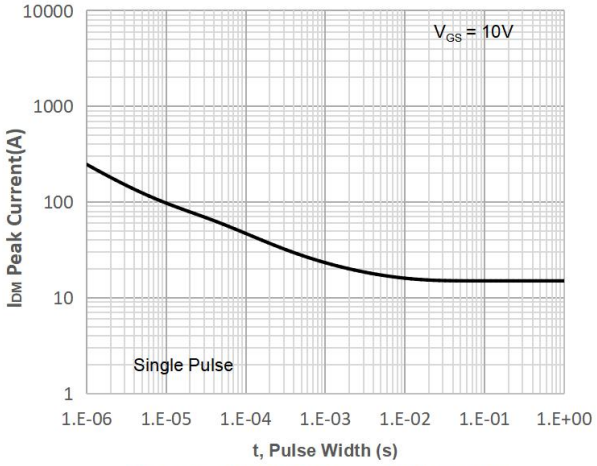
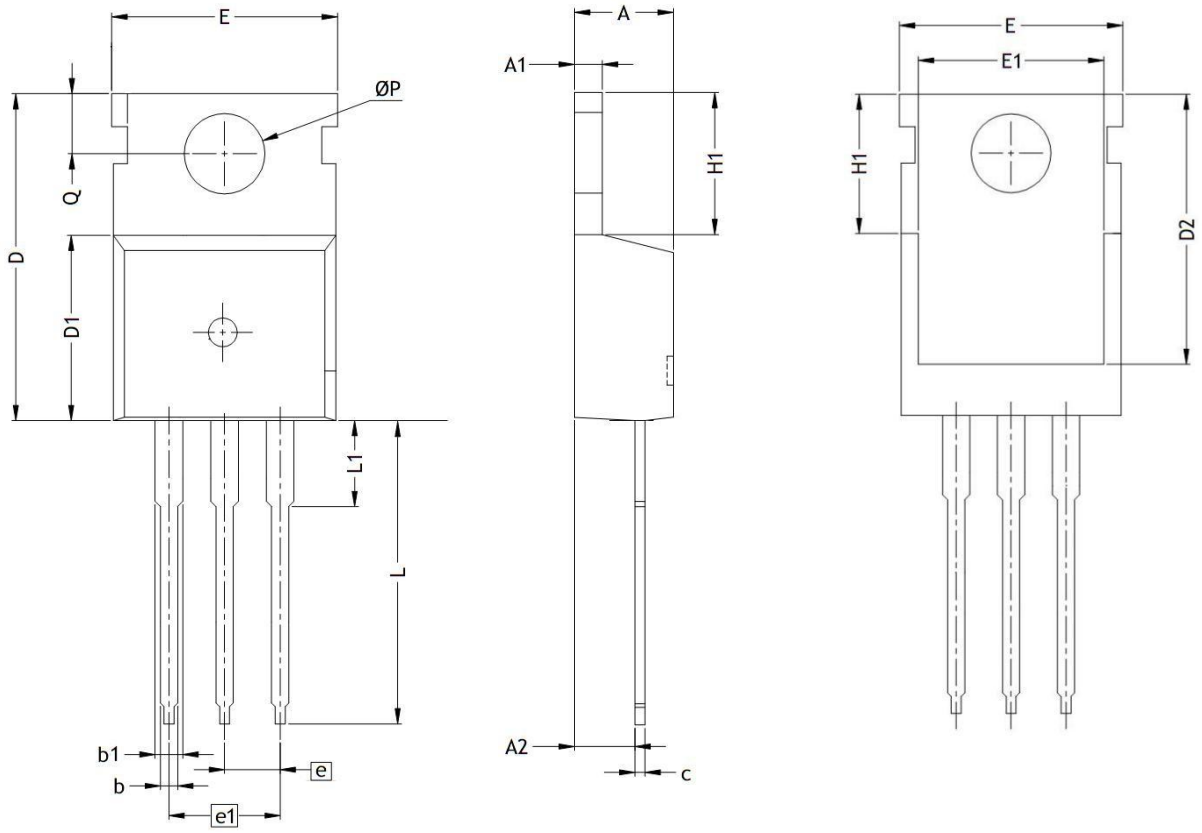


Figure 12: Peak Current Capacity

TO-220 Package Information：



UNIT：mm

SYMBOLS	A	A1	A2	b	b1	c	D	D1	D2	E	E1	e
MIN	4.25	1.25	2.35	0.7	1.15	0.45	14.35	8.80	13.05	9.90	7.85	2.540
MAX	4.65	1.35	2.55	0.9	1.75	0.60	15.95	9.50	13.65	10.35	8.85	BSC
SYMBOLS	e1	H1	L	L1	Q	ϕP						
MIN	5.080	6.30	12.85	2.85	2.70	3.50						
MAX	BSC	6.65	13.50	3.25	2.90	3.70						

Marking Information：

①. Doingter LOGO

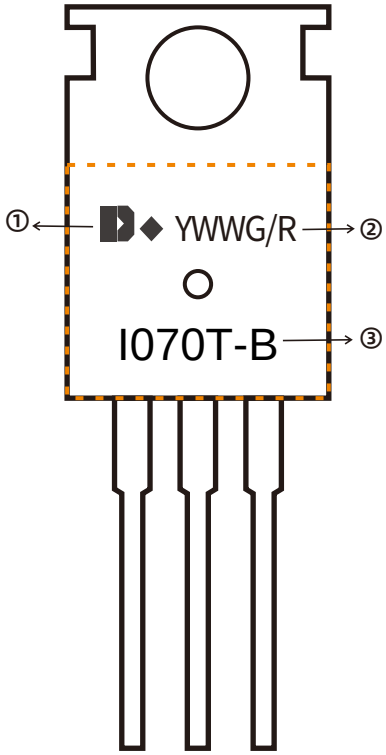
②. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)

③. Part NO.



Previous Version

Version	Date	Subjects (major changes since last revision)
1.1	2025-10-10	Release of final version

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