PH955L,115-VB Datasheet

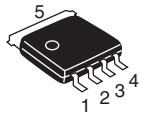
N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	60
$R_{DS(on)}$ (Ω) at V_{GS} = 10 V	0.0050
$R_{DS(on)}$ (Ω) at V_{GS} = 4.5 V	0.0055
I_D (A)	97
Configuration	Single

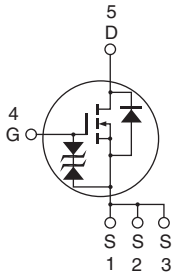
FEATURES

- Trench Power MOSFET
- Package with Low Thermal Resistance
- 100 % R_g and UIS Tested

RoHS COMPLIANT
HALOGEN FREE



1, 2, 3 Source
4 Gate
5 Drain



ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	T_C = 25 °C	I_D	97	A
	T_C = 125 °C		56	
Continuous Source Current (Diode Conduction)[a]		I_S	100	
Pulsed Drain Current[b]		I_{DM}	290	
Single Pulse Avalanche Current	L = 0.1 mH	I_{AS}	45	
Single Pulse Avalanche Energy		E_{AS}	101	mJ
Maximum Power Dissipation[b]	T_C = 25 °C	P_D	136	W
	T_C = 125 °C		45	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount[c]	R_{thJA}	50	°C/W
Junction-to-Case (Drain)		R_{thJC}	1.1	

Notes

a. Package limited.
b. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
c. When mounted on 1" square PCB (FR-4 material).
d. Parametric verification ongoing.

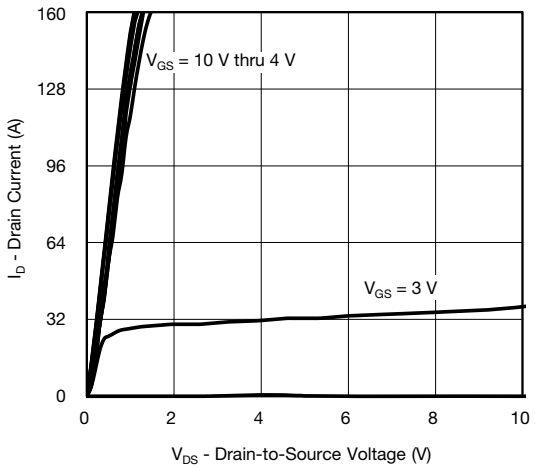
SPECIFICATIONS (T_C = 25 °C, unless otherwise noted)							
PARAMETER	**SYMBOL**	**TEST CONDITIONS**		**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static							
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA		60	-	-	V
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA		1.5	2.0	2.5	
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{GS} = 0 V	V_{DS} = 60 V	-	-	1	µA
		V_{GS} = 0 V	V_{DS} = 60 V, T_J = 125 °C	-	-	50	
		V_{GS} = 0 V	V_{DS} = 60 V, T_J = 175 °C	-	-	150	
On-State Drain Current[a]	$I_{D(on)}$	V_{GS} = 10 V	V_{DS} ≥ 5 V	50	-	-	A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V	I_D = 25 A	-	0.0050	-	Ω
		V_{GS} = 10 V	I_D = 25 A, T_J = 125 °C	-	0.0117	-	
		V_{GS} = 10 V	I_D = 25 A, T_J = 175 °C	-	0.0149	-	
		V_{GS} = 4.5 V	I_D = 20 A	-	0.0055	-	
Forward Transconductance[b]	g_{fs}	V_{DS} = 15 V, I_D = 25 A		-	177	-	S
Dynamic[b]							
Input Capacitance	C_{iss}	V_{GS} = 0 V	V_{DS} = 25 V, f = 1 MHz	-	4844	6060	pF
Output Capacitance	C_{oss}			-	441	555	
Reverse Transfer Capacitance	C_{rss}			-	200	250	
Total Gate Charge[c]	Q_g	V_{GS} = 10 V	V_{DS} = 30 V, I_D = 50 A	-	82	125	nC
Gate-Source Charge[c]	Q_{gs}			-	14.5	-	
Gate-Drain Charge[c]	Q_{gd}			-	13.5	-	
Gate Resistance	R_g	f = 1 MHz		1	2	3	Ω
Turn-On Delay Time[c]	$t_{d(on)}$	V_{DD} = 30 V, R_L = 0.6 Ω I_D ≅ 50 A, V_{GEN} = 10 V, R_g = 1 Ω		-	14	21	ns
Rise Time[c]	t_r			-	5	8	
Turn-Off Delay Time[c]	$t_{d(off)}$			-	41	62	
Fall Time[c]	t_f			-	7	11	
Source-Drain Diode Ratings and Characteristics[b]							
Pulsed Current[a]	I_{SM}			-	-	290	A
Forward Voltage	V_{SD}	I_F = 50 A, V_{GS} = 0 V		-	0.9	1.5	V

Notes

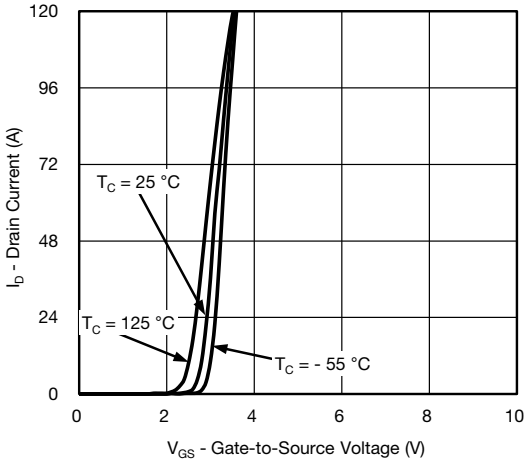
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

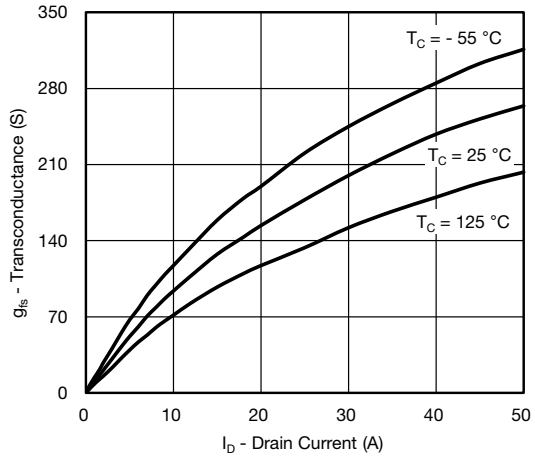
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



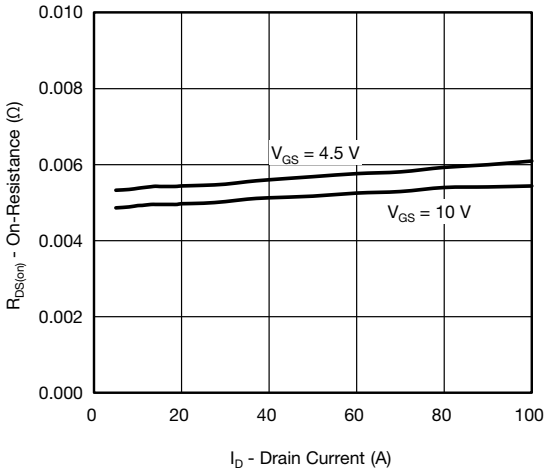
Output Characteristics



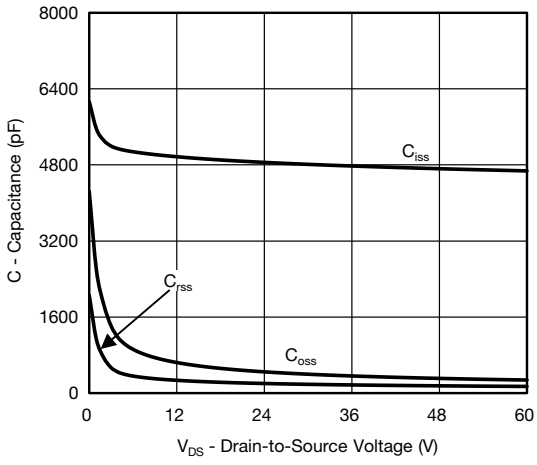
Transfer Characteristics



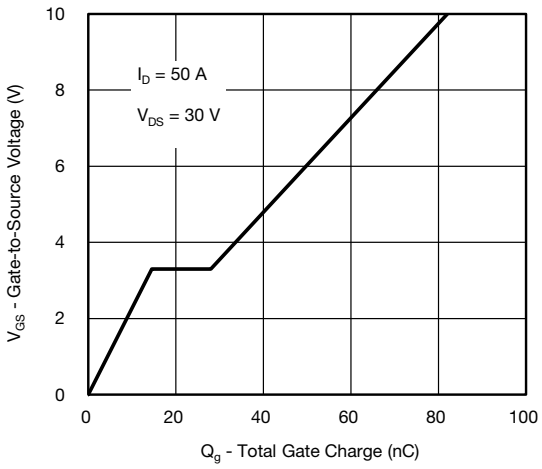
Transconductance



On-Resistance vs. Drain Current

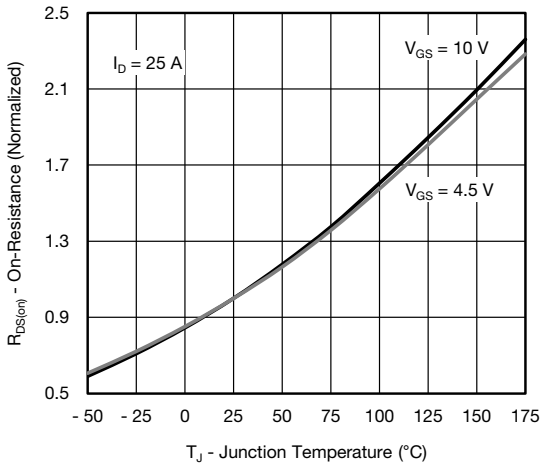


Capacitance

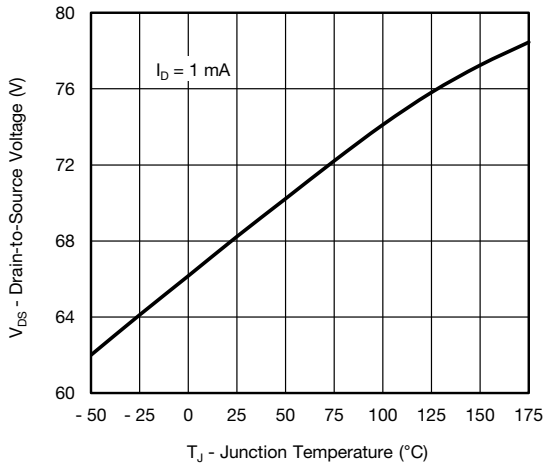


Gate Charge

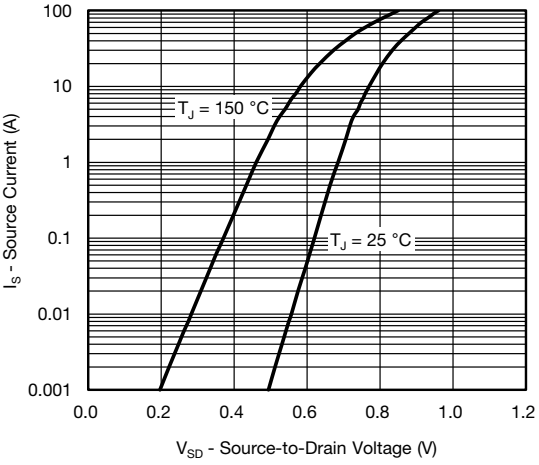
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



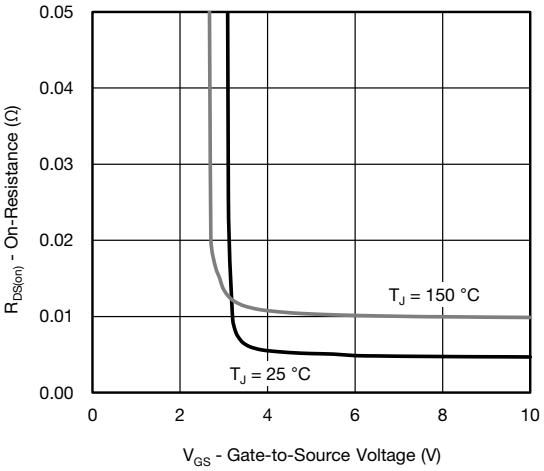
On-Resistance vs. Junction Temperature



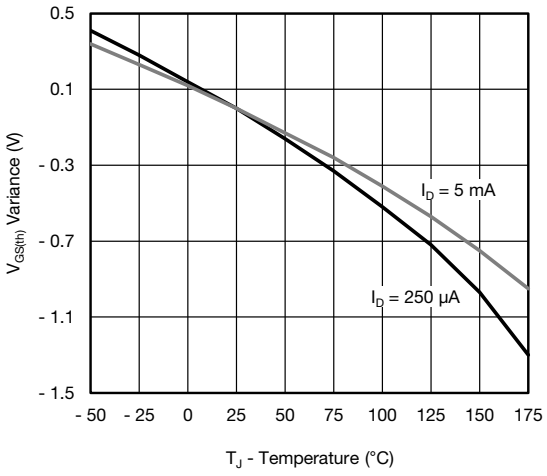
Drain Source Breakdown vs. Junction Temperature



Source Drain Diode Forward Voltage

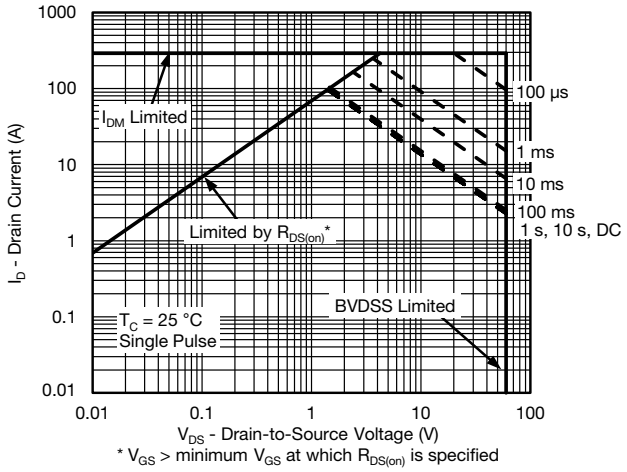


On-Resistance vs. Gate-to-Source Voltage

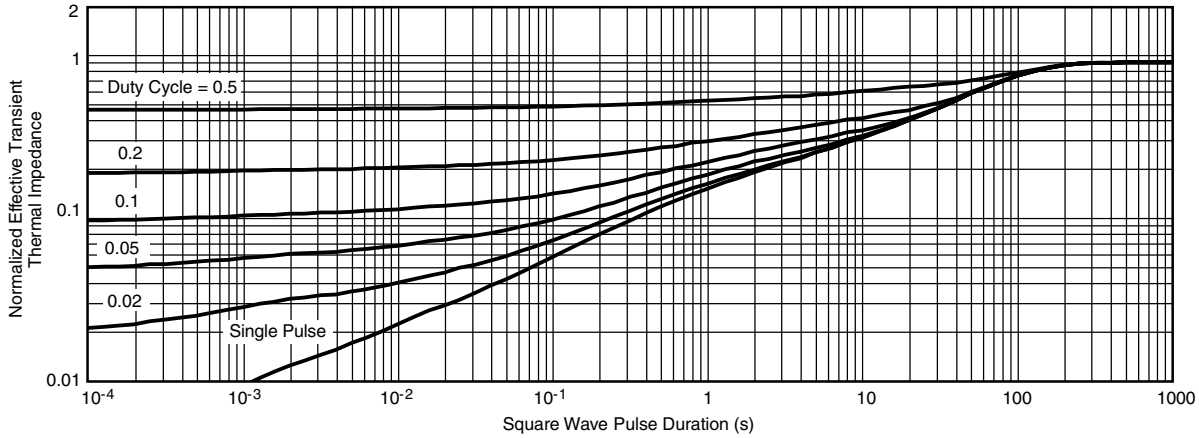


Threshold Voltage

THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)

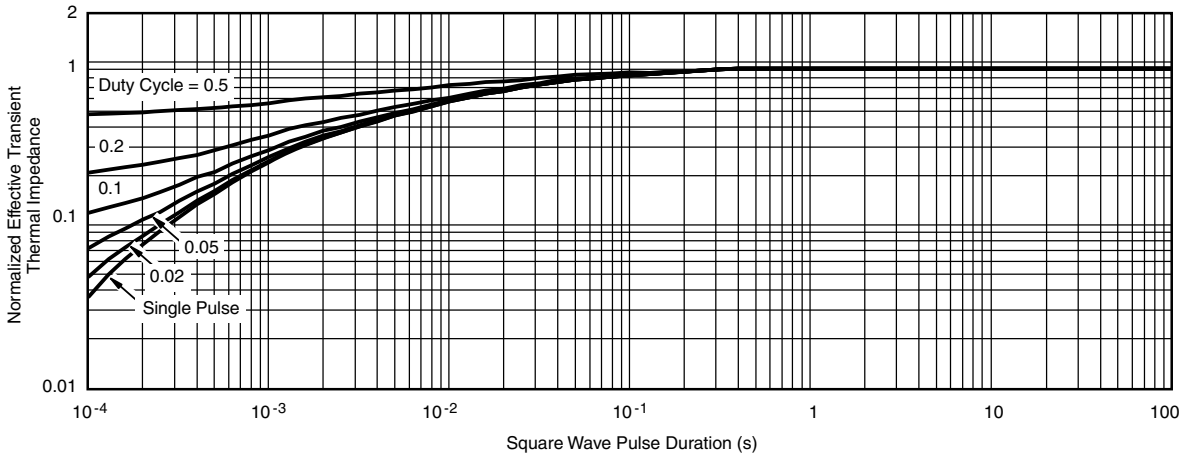


Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



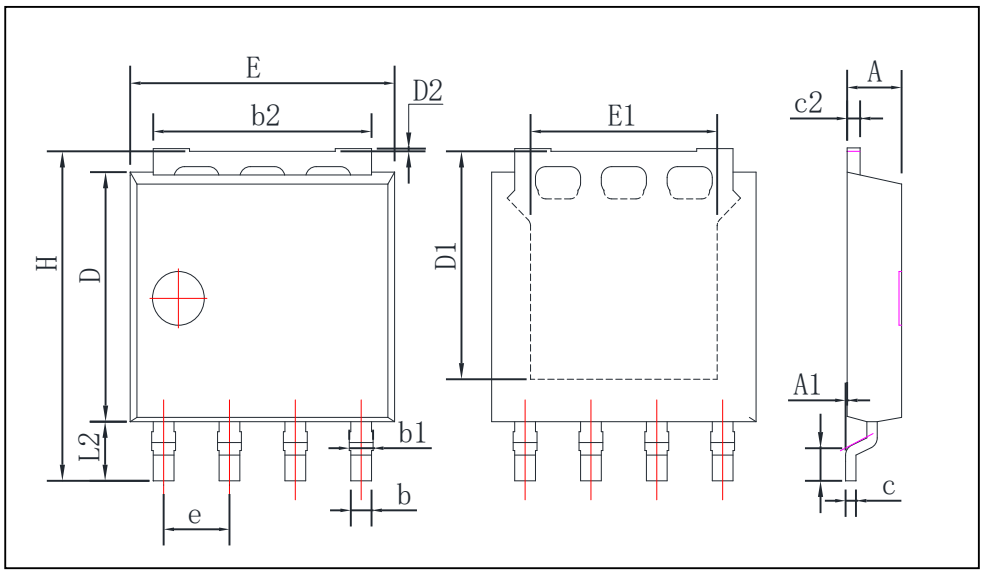
Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

  are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

LFPAK56 CASE OUTLINE



Symbol	Min	Typ	Max
A	1.00	1.05	1.10
A1	0.00		0.15
b	0.35	0.40	0.50
b1	0.40	0.48	0.58
b2	4.01	4.21	4.41
c	0.18	0.20	0.25
c2	0.23	0.25	0.30
D	4.44	4.59	4.70
D1	4.10	4.24	4.40
D2			0.20
e	1.22	1.27	1.32
E	5.00	5.10	5.25
E1	3.50	3.60	3.70
H	6.05	6.15	6.25
L	0.40	0.60	0.80
L2	0.90	1.10	1.30

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