Surge arrester

2-electrode arrester

Series/Type:	**M51-C90XSMD**
Ordering code:	**B88069X4760T902**
Date:	2015-07-30
Version:	06

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Surge arrester — B88069X4760T902

2-electrode arrester — M51-C90XSMD

Features

- Small size
- High current rating
- Very fast response time
- Stable performance over life
- Very low capacitance
- High insulation resistance
- Excellent SMD handling
- RoHS-compatible

Applications

- Modem
- XDSL-splitter
- Data lines
- Tuner
- Antenna

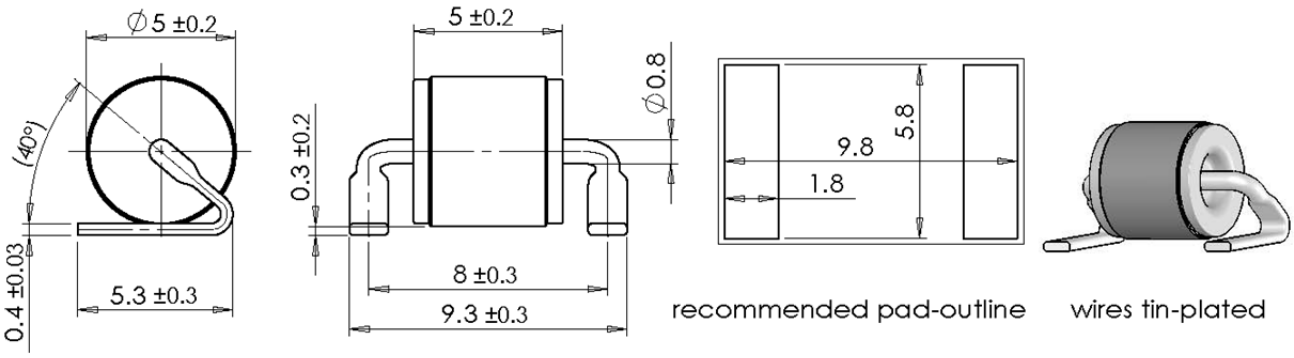
Electrical specifications

DC spark-over voltage [1) 2)]	90	V
Tolerance	± 20	%
Min.	72	V
Max.	108	V
Impulse spark-over voltage		
at 100 V/µs - for 99% of measured values	< 450	V
- typical values of distribution	< 350	V
at 1 kV/µs - for 99% of measured values	< 600	V
- typical values of distribution	< 550	V
Service life		
10 operations 50 Hz, 1 s	5	A
1 operation 50 Hz, 0.18 s (9 cycles)	10	A
10 operations 8/20 µs	5	kA
1 operation 8/20 µs	10	kA
1 operation 10/350 µs	1	kA
Insulation resistance at 50 V_{DC}	> 1	GΩ
Capacitance at 1 MHz	< 1	pF
Arc voltage at 1 A	~ 15	V
Glow to arc transition current	~ 0.8	A
Glow voltage	~ 60	V
Weight	~ 1	g
Operation and storage temperature	–40 ... +90	°C
Climatic category (IEC 60068-1)	40/090/21	
Marking blue negative	EPCOS 90 YY O 90 - Nominal voltage YY - Year of production O - Non radioactive	
Certification	UL 497B (E163070)	

1) At delivery AQL 0.65 level II, DIN ISO 2859
2) In ionized mode

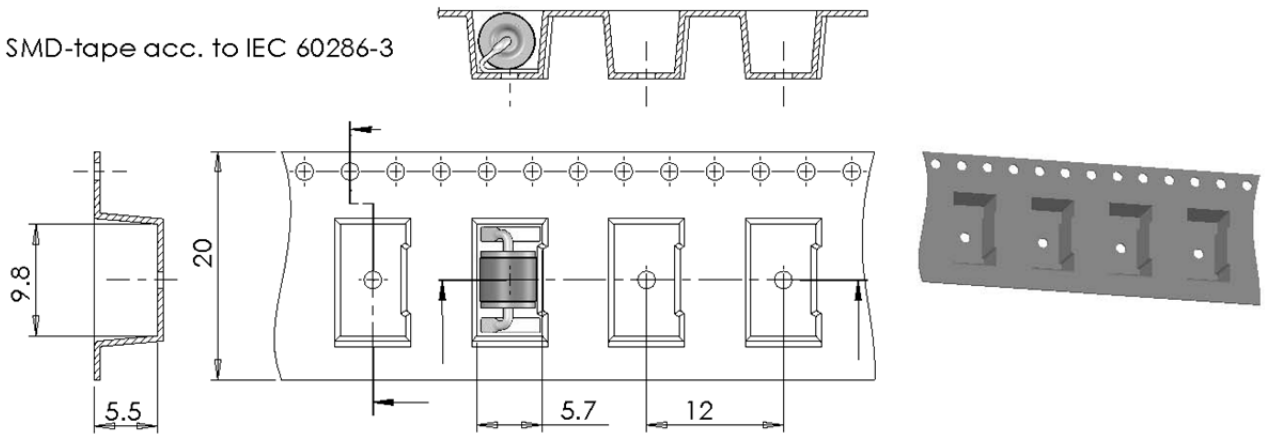
Terms in accordance with ITU-T Rec. K.12, IEC 61663-2 and IEC 61643-311

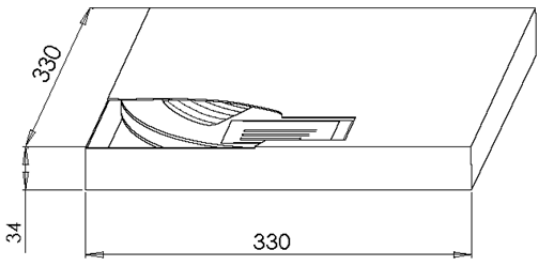
Dimensional drawing in mm



Ordering code and packing advice

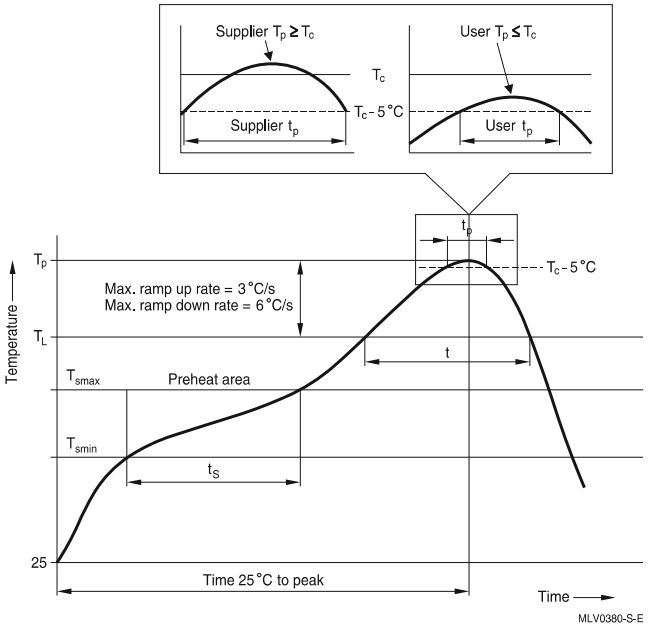
*B88069X4760**T902** = 900 pcs. on SMD-tape*





Soldering parameter

Reflow soldering



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly
Preheat and soak - Temperature min - Temperature max - Time	 T_{smin} T_{smax} t_{smin} to t_{smax}	 100 °C 150 °C 60 ... 120 s	 150 °C 200 °C 60 ... 180 s
Average ramp-up rate	T_{smax} to T_p	max. 3 °C/ s	max. 3 °C/ s
Liquidous temperature Time at liquidous	T_L t_L	183 °C 60 ... 150 s	217 °C 60 ... 150 s
Peak package body temperature *, Classification temperature **	T_p, T_C	220 ... 235 °C **	245 ... 260 °C **
Time (t_p) ** within 5 °C of the specified classification temperature (T_C)		20 s ***	30 s ***
Average ramp-down rate	T_p to T_{smax}	max. 6 °C/ s	max. 6 °C/ s
Time 25 °C to peak temperature		max. 6 min	max. 8 min

* = Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.
** = For details please refer to JEDEC J-STD-020D.
*** = Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

Cautions and warnings

- Do not operate surge arresters in power supply networks, whose maximum operating voltage exceeds the minimum spark-over voltage of the surge arresters.
- Surge arresters may become hot in the event of longer periods of current stress (burn risk). In the event of overload the connectors may fail or the component may be destroyed.
- Surge arresters must be handled with care and must not be dropped.
- Do not continue to use damaged surge arresters.
- The shown SMD pad dimensions represent a safe way to mount the arrester and are a recommendation of the manufacturer. During the reflow process it must be assured that no solder material reduces the insulation distance between the pads below the arrester.
- SMD surge arresters should be soldered within 24 month after shipment.

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Release 2018-10

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