GENERAL DESCRIPTION

The FP6291LR-G1 is a constant frequency,6-pin SOT23 current mode step-up converter intended for small,low power applications.The FP6291LR-G1 switches at 1.2MHz and allows the use of tiny,low cost capacitors and inductors 2mm or less in height. Internal soft-start results in small inrush current and extends battery life.

The FP6291LR-G1 features automatic shifting to pulse frequency modulation mode at light loads. The FP6291LR-G1 includes under-voltage lockout,current limiting,and thermal overoad protection to preve nt damage in the event of an output overload. The FP6291LR-G1 is available in a small 6-pin SOT-23 package.

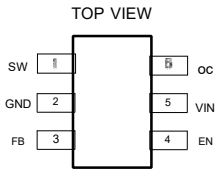
Features

- Integrated 80mQ Power MOSFET
- 2.2V to 16V Input Voltage
- 1.2MHz Fixed Switching Frequency
- Adjustable Over Current Protection:0.5A ~2.5A
- Internal 2.5A Switch Current Limit(0C pin floating)
- Adjustable Output Voltage
- Internal Compensation
- Over Voltage Protection
- Up to 20V Output Voltage
- Automatic Pulse Frequency Modulation Mode at Light Loads
- up to 93%Efficiency
- Available in a 6-Pin SOT23-6 Package

Applications

- Battery-Powered Equipment
- Set-Top Boxed
- LCD Bais Supply
- DSL and Cable Modems and Routers
- Networking cards powered from PCI or PCI express slots

Pin Description AND MARKING

Pin Configuration	Marking
	

device code: A17, ***= Internal production code

Pin Description

Pin Name	Pin Number	Description
SW	1	Power Switch Output.SW is the drain of the internal MOSFET switch. Connect the power inductor and output rectifier to SW.SW can swing between GND and 22V.
GND	2	Ground Pin
FB	3	Feedback Input.The FB voltage is 0.6V.Connect a resistor divider to FB.
EN	4	Regulator On/Off Control Input.A high input at EN turns on the converter,and a low input turns it of.When not used,connect EN to the input supply for automatic startup.
VIN	5	Input Supply Pin.Must be locally bypassed.
0C	6	Adjustable Current Limit (Floating Available).

Order Information

Model	Description	Package	MOQ
FP6291LR-G1	T_{JMAX}= 160℃, θ_{JA}=250℃/W, θ_{JC}= 130℃/W	SOT23-6	3000

Typical Application Circuit

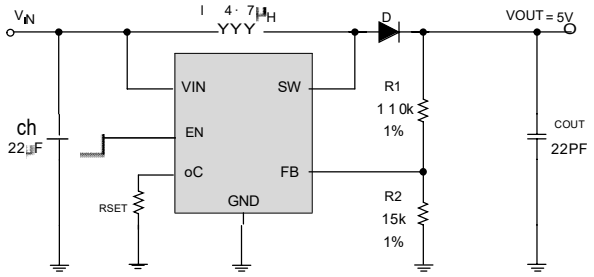


Figure 1. Basic Application circuit

ABSOLUTE MAXIMUM RATINGS (Note1)

VIN , EN Voltages .....-0.3v to 18v
FB , voltages ..... -0.3v to 6v
sw voltage .....-0.3v to 22v
power Dissipation 0.6w
Thermal Resistance θJC 130c/w
Thermal Resistance θJA 250c/w
Junction Temperature(Note2) .....160。c
operating Temperature Range .....-40c to 85。c
Lead Temperature(soldering, 10s) 300ec
storage Temperature Range .....-65。c to 150c
ESD HBM(Human Body Mode) 2kv
ESD MM(Machine Mode) 200v

ELECTRICAL CHARACT ERISTICS Note3)

VN = VEN = 5v,TA = 25 C, unless otherwise noted.)

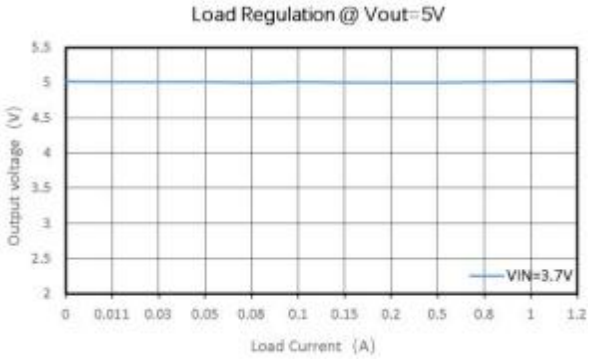
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
operating Input voltage		2.2		16	V
under voltage Lockout				2.2	V
under voltage Lockout Hysteresis			100		mv
current (shutdown)	VEN = oV		0.1	1	μA
Quescent current (PFM)	VFB = . V, No sWtch		100	200	μA
Quiescent current (PWM)	VFB = 0.5V, switch		1.6	2.2	mA
switching Frequency			1.2		MHz
Maxmum Duty cyce	VFB = oV	90			%
EN Input High voltage		1.5			V
EN Input Low voltage				0.6	V
ovp with output			20		V
FB voltage		0.588	0.6	0.612	V
FB Input Bias current	VFB = 0.6V	-50	-10		nA
sw on Resistance			80	150	mQ
sw current Limit	VN = 5v, Duty cycle = 50%, oc floating		2.5		A
Adustable ocp current	with External Resistor:19k~96k	0.5		2.5	A
Sw Leakage	VSW = 20V			1	μA
Thermal shutdown			160		℃
Thermal shutdown Hysteresis			25		℃

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device maybe impaired.
Note 2: TJ is Calculated from the ambient temperature TA and power dissipation PD according to the following formula: TJ = TA + (PD) x (250c/w).

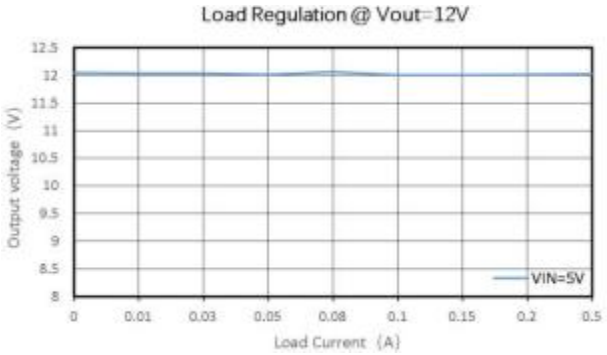
Note 3: 100% production test at25。c. specifications over the temperature range are guaranteed by design and characterization.

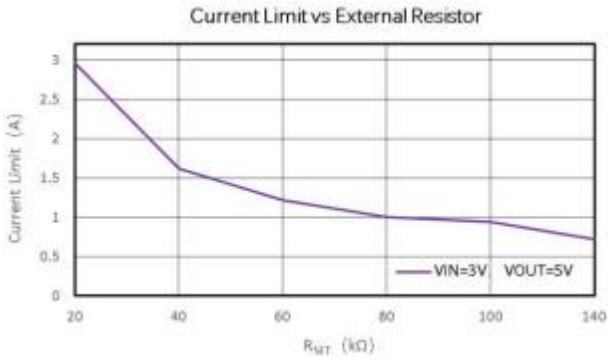
TYPICAL PERFORMANCE CHARACTERISTICS

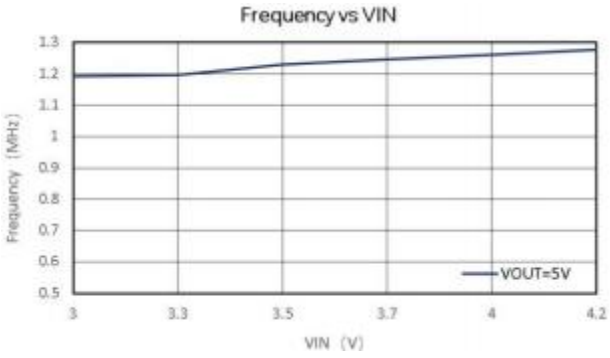




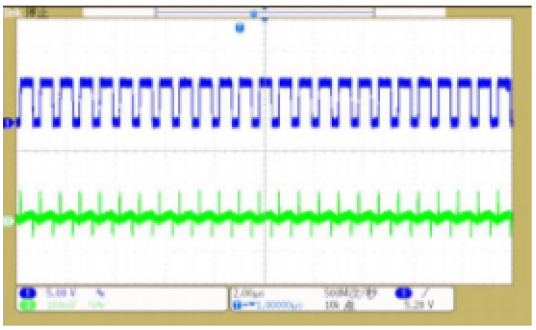




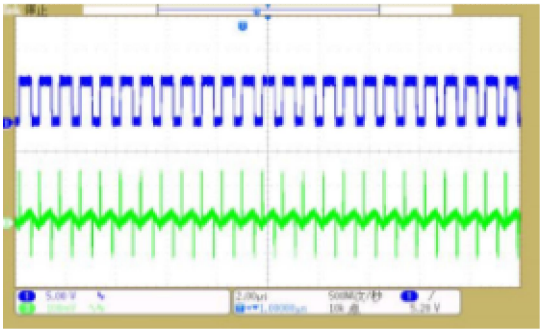




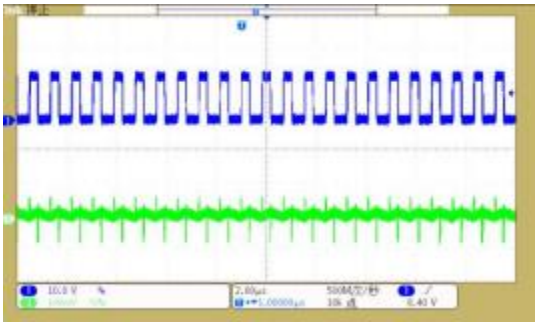
$V_{IN}=3.7V, V_{OUT}=5V, I_{LOAD}=0.5A$



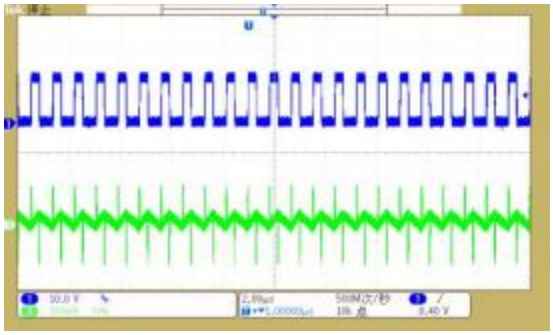
$V_{IN}=3.7V\ V_{OUT}=5V\ I_{LOAD}=1A$



VIN = 5V, VOUT = 12V, ILOAD = 0 . 2A



VIN = 5V, VOUT = 12V, ILOAD = 0 . 5A



FUNCTIONAL BLOCK DIAGRAM

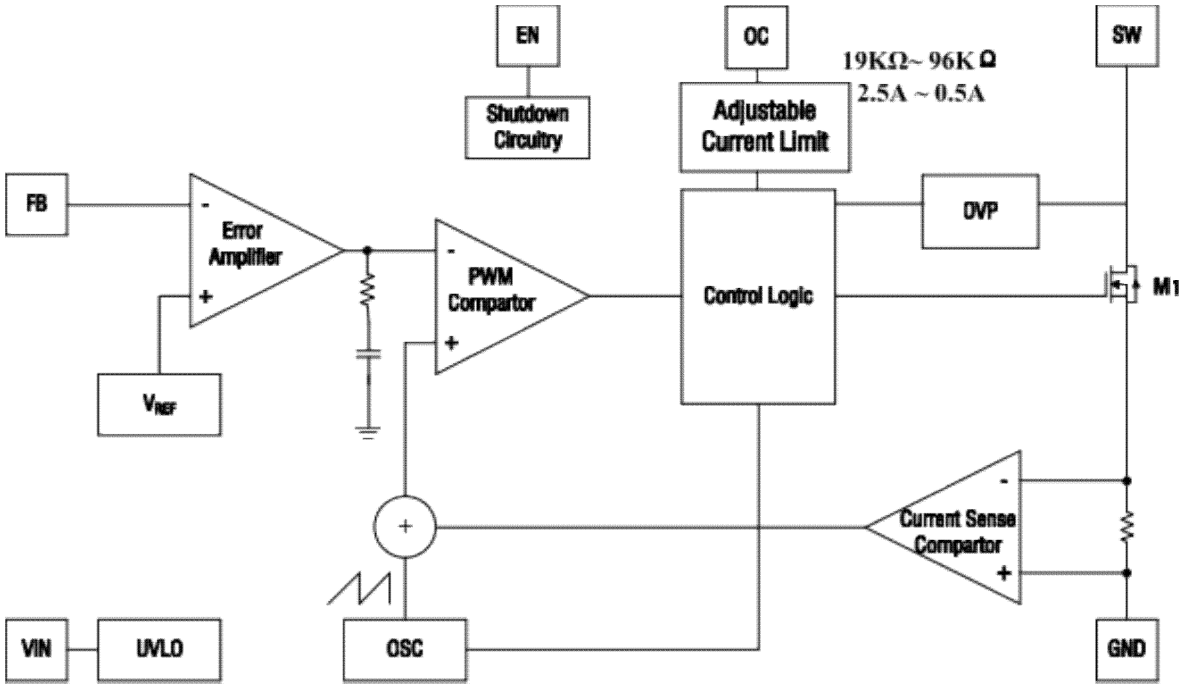


Figure 2· FP6291LR-G1 Block Diagram

FUNCTIONAL DESCRIPTION

overview

The FP6291LR - G1 uses a fixed frequency, peak current mode boost regulator architect ure to regulate voltage atthe feedback pin. The o peration of the FP6291LR - G1 can be underst ood by referring to the block diagram of Figure 2. At the start of each oscillator cycle the M OSFET is turned on through the control circu itry. To prevent sub- harmonic oscillations at duty cycles greater than50 percent, a stabiliz ing ramp is added to the output of the current sense amplifier and the result is fed into the negative input of the PWM Comparator. W hen this voltage equals The output voltage of the error amplifier the power MOSFET is turn ed off. The voltage atthe output of the error am plifier is an amplified version of the difference between the 0.6v bandgap reference voltage and the feedback voltage. In this way the peak current level keeps the output in regulation. If the feedback voltage starts to drop, the output of the error amplifier increases. These results in more current to flow through the power MOSFET, thus increasing the power delivered to the output. The FP6291LR - G1 has internal soft start to limit the amount of input current at startup and to also limit the amount of overshoot on the output.

cument Limitprogram

A resistor between oc and GND Pin programs peak switch current· The resistor value should be between 19k and 96k. The current limit will be set from 2.5A to 0.5A. keep traces at this pin as short as possible. The oc pin can be floating, the current limit will be set by Internal 2.5A Current limit. To set the over current trip point according to the following equation:

locp=48000/R3

overvoltage protection (0vp)

In some condition, the resistive divider may be unconnected, which will cause PWM Signal to operate with maximum duty cycle and output voltage is boosted higher and higher. The power MOSFET Will be turned off immediately, when the output voltage exceeds the ovp threshold level. The FP6291 LR - G1 's ovp threshold is 20v.

APPUICATIONS INFORMATION

setting the outputvoltage

The internal reference VREF is 0.6v (Typical).The output voltage is divided by a resistor divider, R1 and R2 to the FB Pin. The output voltage is given by

$$V_{OUT} = V_{REF} \times \left(1 + \frac{R_1}{R_2}\right)$$

Inductorselection

The recommended values of inductor are 4.7 to 22UH. small size and better efficiency are the major concerns for portable device, such as FP6291LR - G1 used for mobile phone. The inductor should have low core loss at 1.2MHz and low DCRfor better efficiency.To avoid inductor saturation current rating should be considered.

capacitorselection

Input and output ceramic capacitors of 22UF are recommended for FP6291LR - G1 applications For better voltage filtering, ceramic capacitors with low ESR are recommended. X5R and X7R types are suitable because of their wider voltage and temperature ranges.

Diode selection

schottky diode is a good choice for FP6291LR -G1 because of its low forward voltage drop and fast

reverses recovery. using schottky diode can get better efficiency. The high speed rectification is also a good characteristic of schottky diode for high switching frequency. current rating of the diode must meet the root mean square of the peak current and output average current multiplication as following

$$I_D(RMS) \approx \sqrt{I_{OUT} \times I_{PEAK}}$$

The diode' s reverse breakdown voltage should be larger than the output voltage.

Layout consideration

For best performance of the FP6291LR - G1 , the following guidelines must be strictly followed.

- Input and output capacitors should be placed close to the IC and connected to ground plane to reduce noise coupling.
- The GND Should be connected to a strong ground plane for heat sinking and noise protection.
- keep the main current traces as possible as short and wide.
- Sw node of DC-DC Converter is with high frequency voltage swing. It should be kept at a small area.
- place the feedback components as close as possible to the IC and keep away from the noisy devices.

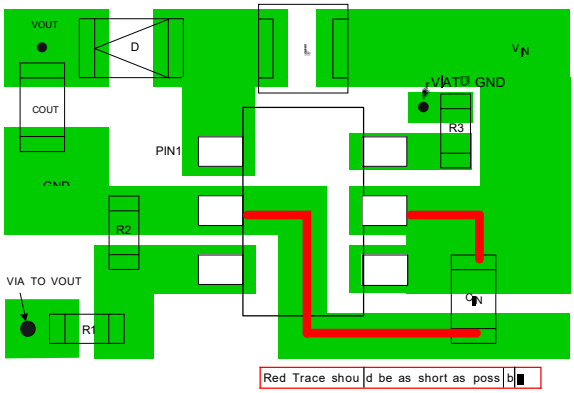
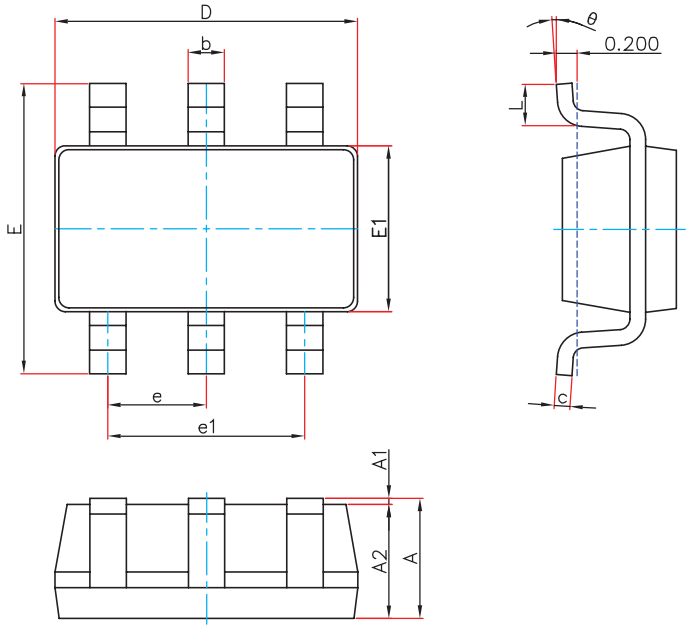


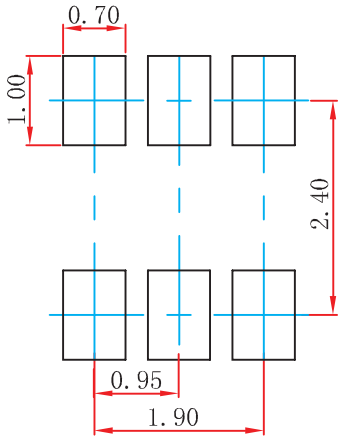
Figure 3. FP6291LR - G1 suggested Layout

package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

SOT-23-6 Suggested pad Layout



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:± 0.05mm.
3.The pad layout is for reference purposes only.