## Discription

The AOZ8S502BDS-03 protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.




DFN1006-2L

## Features

- ★ Small Body Outline Dimensions
- ★ Low Body Height
- ★ Peak Power up to 105 Watts @ 8 x 20μs Pulse
- ★ Low Leakage current
- ★ Response Time is Typically < 1 ns
- ★ ESD Rating of Class 3 per Human Body Model




Circuit Diagram

## Ordering Information

| Product ID | Pack | Qty(PCS) |
|---|---|---|
| AOZ8S502BDS-03 | DFN1006-2L | 10000 |

## Absolute Ratings(Tamb = 25°C)

| Symbol | Parameter | Value | Units |
|---|---|---|---|
| $P_{PP}$ | Peak Pulse Power ($t_p$ = 8/20μs) | 100 | W |
| $T_L$ | Maximum lead temperature for soldering during 10s | 260 | °C |
| $T_{stg}$ | Storage Temperature Range | -55 to +150 | °C |
| $T_{op}$ | Operating Temperature Range | -55 to +150 | °C |
| $T_j$ | Maximum junction temperature | 150 | °C |
| | IEC61000-4-2 (ESD) air discharge<br>contact discharge | ±30<br>±30 | KV |
| | IEC61000-4-4 (EFT) | 40 | A |

## Electrical Characteristics

| Device | $V_{RWM}$ (V) | $I_R$ (μA) @ $V_{RWM}$ | $V_{BR}$ (V) @ $I_T$ (Note 1) | | $I_T$ | $V_C$ (V) @ $I_{PP}$ = 1 A (Note 2) | $V_C$ (V) @MAX $I_{PP}$ (Note 2) | $I_{PP}$(A) (Note 2) | $P_{PK}$(W) (Note 2) | C (pF) | $R_{DYN}$(Ω) @$t_p$=100 ns(TLP) |
|---|---|---|---|---|---|---|---|---|---|---|---|
| | Max | Max | Min | Max | mA | Max | Max | Max | Max | Typ. | Typ. |
| AOZ8S502BDS-03 | 3.3 | 0.05 | 5 | 6.5 | 1.0 | 7 | 11 | 9 | 100 | 15 | 0.2 |

1. $V_{BR}$ is measured with a pulse test current $I_T$ at an ambient temperature of 25°C.
2. Surge current waveform per Figure 1.

## Typical Characteristics

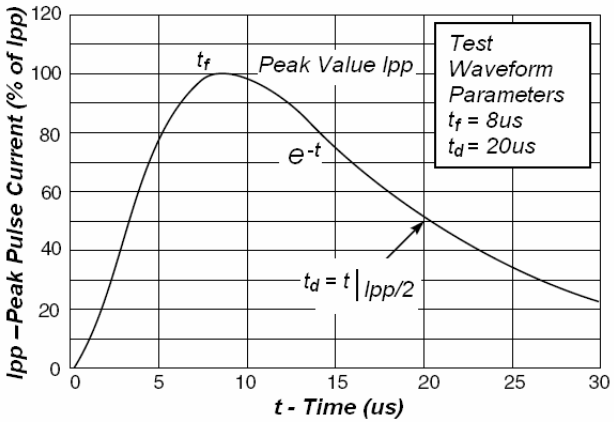



*Fig1. Pulse Waveform*

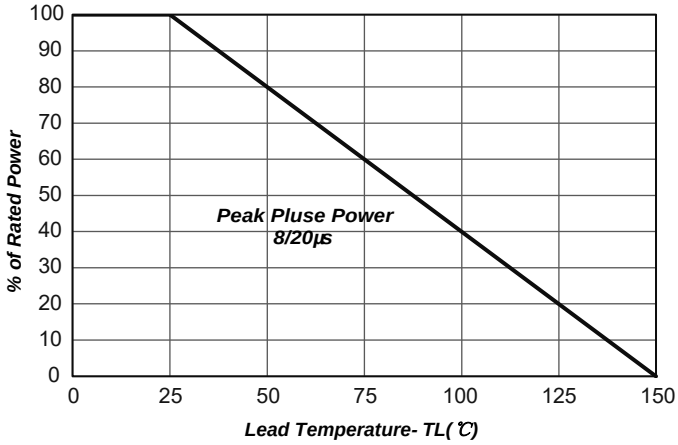



*Fig2.Power Derating Curve*

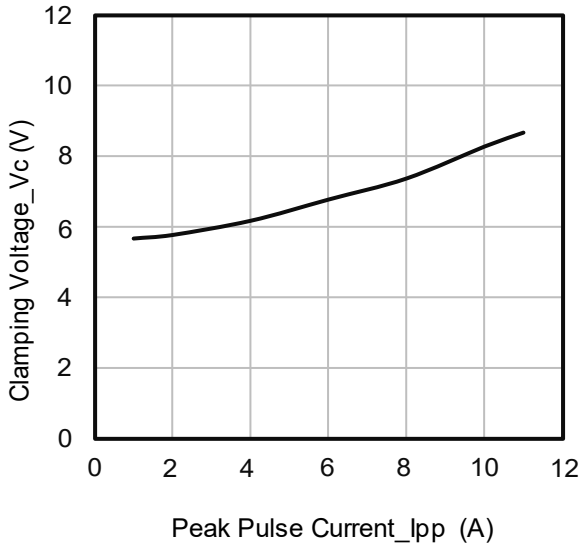



*Fig3. Clamping Voltage vs. Peak Pulse Current*

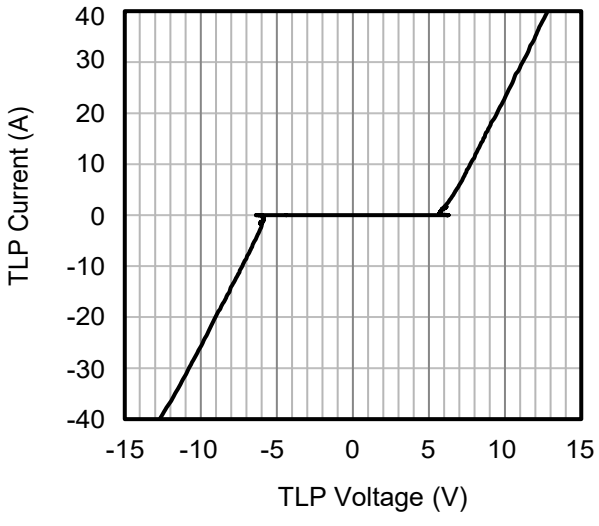
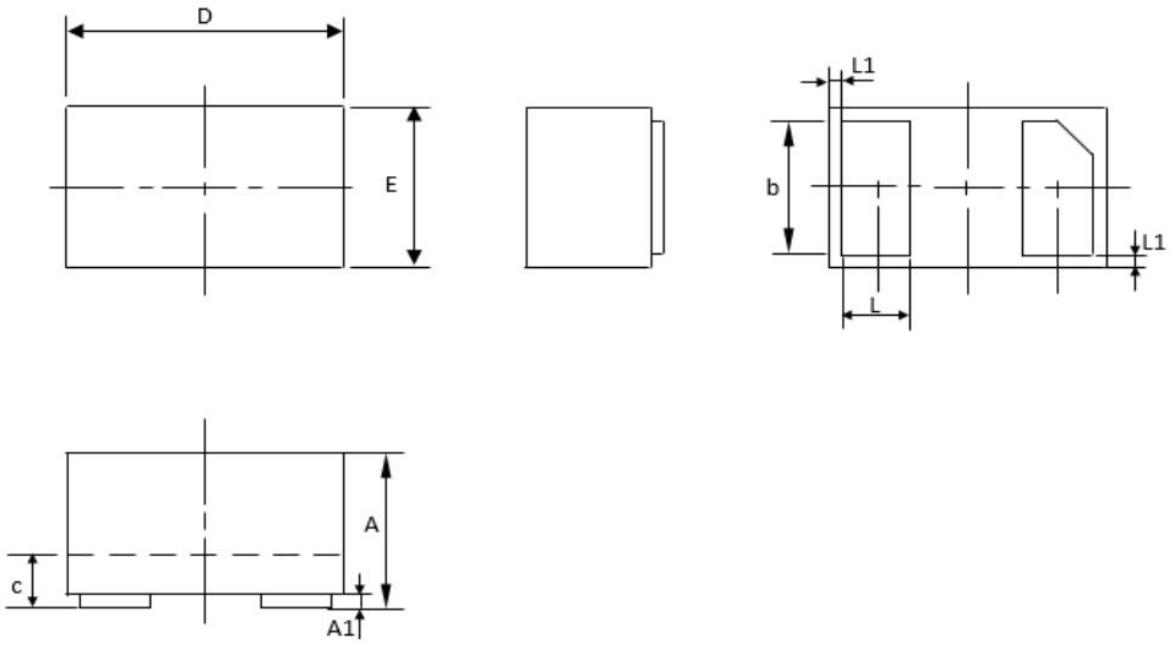



*Fig4. TLP Measurement*

## Outline And Dimensions




| DFN1006-2L | | | |
|---|---|---|---|
| Dim | Min | Typ. | Max |
| A | 0.46 | 0.48 | 0.50 |
| A1 | 0 | 0.02 | 0.05 |
| b | 0.45 | 0.5 | 0.55 |
| c | 0.1 | 0.12 | 0.14 |
| D | 0.95 | 1.00 | 1.05 |
| E | 0.55 | 0.60 | 0.65 |
| L | 0.20 | 0.25 | 0.30 |
| L1 | 0.035 | 0.05 | 0.065 |
| h | 0.07 | 0.12 | 0.17 |
| All Dimensions in mm | | | |

## Attention

■ Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.

■ HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.

■ Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer’s products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer’s products or equipment.

■ HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could
give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

■ In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.

■ Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.