# TPH3R70APL,L1Q(M-VB Datasheet

## N-Channel 100V (D-S) MOSFET

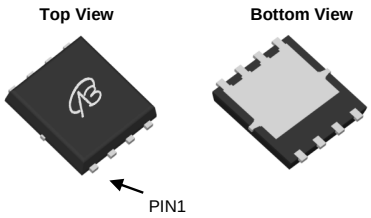
| PRODUCT SUMMARY | | |
|---|---|---|
| $V_{DS}$ (V) | $R_{DS(on)}$ (Ω) | $I_D$ (A)[a] |
| 100 | 0.003 at $V_{GS}$ = 10 V | 180 |
| | 0.004 at $V_{GS}$ = 4.5 V | 175 |

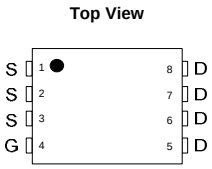
### FEATURES

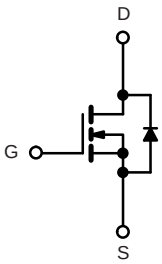
- 175 °C Junction Temperature
- SGT technology Power MOSFET
- Material categorization:

RoHS COMPLIANT

DFN5X6




N-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS ($T_C$ = 25 °C, unless otherwise noted) | | | | |
|---|---|---|---|---|
| **Parameter** | | **Symbol** | **Limit** | **Unit** |
| Gate-Source Voltage | | $V_{GS}$ | ±20 | V |
| Continuous Drain Current ($T_J$ = 175 °C)[b] | $T_C$ = 25 °C | $I_D$ | 180 | A |
| | $T_C$ = 100 °C | | 120[a] | |
| Pulsed Drain Current | | $I_{DM}$ | 540 | |
| Continuous Source Current (Diode Conduction) | | $I_S$ | 140[a] | |
| Avalanche Current | | $I_{AS}$ | 60 | |
| Single Avalanche Energy (Duty Cycle ≤ 1 %) | L = 0.5 mH | $E_{AS}$ | 900 | mJ |
| Maximum Power Dissipation | $T_C$ = 25 °C | $P_D$ | 176 | W |
| | $T_A$ = 25 °C | | 3[b], 8.3[b, c] | |
| Operating Junction and Storage Temperature Range | | $T_J$, $T_{stg}$ | - 55 to 175 | °C |

| THERMAL RESISTANCE RATINGS | | | | | |
|---|---|---|---|---|---|
| **Parameter** | | **Symbol** | **Typical** | **Maximum** | **Unit** |
| Maximum Junction-to-Ambient[a] | t ≤ 10 sec | $R_{thJA}$ | 15 | 18 | °C/W |
| | Steady State | | 40 | 50 | |
| Maximum Junction-to-Case | | $R_{thJC}$ | 0.80 | 0.85 | |

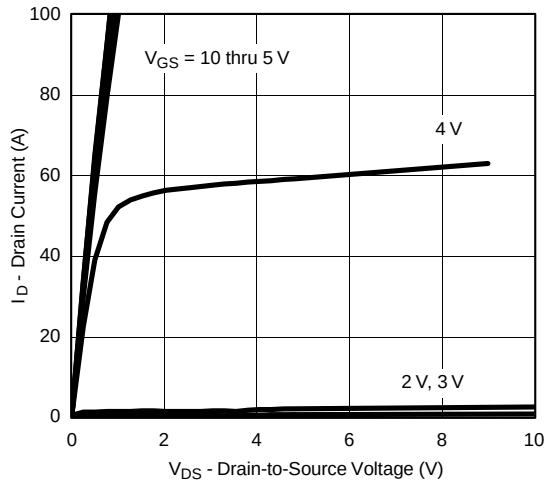
Notes:
a. Package limited.
b. Surface mounted on 1" x 1" FR4 board.
c. t ≤ 10 s.

| **SPECIFICATIONS** ($T_J$ = 25 °C, unless otherwise noted) | | | | | | |
|---|---|---|---|---|---|---|
| **Parameter** | **Symbol** | **Test Conditions** | **Min.** | **Typ.[a]** | **Max.** | **Unit** |
| **Static** | | | | | | |
| Drain-Source Breakdown Voltage | $V_{DS}$ | $V_{GS}$ = 0 V, $I_D$ = 250 µA | 100 | | | V |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}$ = $V_{GS}$, $I_D$ = 250 µA | 1 | 2 | 3 | |
| Gate-Body Leakage | $I_{GSS}$ | $V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V | | | ± 100 | nA |
| Zero Gate Voltage Drain Current | $I_{DSS}$ | $V_{DS}$ = 30V, $V_{GS}$ = 0 V | | | 1 | µA |
| | | $V_{DS}$ = 30V, $V_{GS}$ = 0 V, $T_J$ = 125 °C | | | 50 | |
| | | $V_{DS}$ = 30V, $V_{GS}$ = 0 V, $T_J$ = 175 °C | | | 250 | |
| On-State Drain Current[b] | $I_{D(on)}$ | $V_{DS}$ = 5 V, $V_{GS}$ = 10 V | 60 | | | A |
| Drain-Source On-State Resistance[b] | $R_{DS(on)}$ | $V_{GS}$ = 10 V, $I_D$ = 20 A | | 0.003 | | Ω |
| | | $V_{GS}$ = 10 V, $I_D$ = 20 A, $T_J$ = 125 °C | | 0.004 | | |
| | | $V_{GS}$ = 10 V, $I_D$ = 20 A, $T_J$ = 175 °C | | 0.006 | | |
| | | $V_{GS}$ = 4.5 V, $I_D$ = 35 A | | 0.004 | | |
| Forward Transconductance[b] | $g_{fs}$ | $V_{DS}$ = 15 V, $I_D$ = 20 A | | 60 | | S |
| **Dynamic** | | | | | | |
| Input Capacitance | $C_{iss}$ | $V_{GS}$ = 0 V, $V_{DS}$ = 100 V, f = 1 MHz | | 7600 | | pF |
| Output Capacitance | $C_{oss}$ | | | 470 | | |
| Reverse Transfer Capacitance | $C_{rss}$ | | | 225 | | |
| Total Gate Charge[c] | $Q_g$ | $V_{DS}$ = 100 V, $V_{GS}$ = 10 V, $I_D$ = 50 A | | 60 | 70 | nC |
| Gate-Source Charge[c] | $Q_{gs}$ | | | 16 | | |
| Gate-Drain Charge[c] | $Q_{gd}$ | | | 17 | | |
| Turn-On Delay Time[c] | $t_{d(on)}$ | $V_{DD}$ = 100 V, $R_L$ = 0.6 Ω<br>$I_D$ ≅ 50 A, $V_{GEN}$ = 10 V, $R_g$ = 2.5 Ω | | 20 | 26 | ns |
| Rise Time[c] | $t_r$ | | | 15 | 25 | |
| Turn-Off Delay Time[c] | $t_{d(off)}$ | | | 35 | 50 | |
| Fall Time[c] | $t_f$ | | | 20 | 30 | |
| **Source-Drain Diode Ratings and Characteristics** ($T_C$ = 25 °C) | | | | | | |
| Pulsed Current | $I_{SM}$ | | | | 540 | A |
| Diode Forward Voltage | $V_{SD}$ | $I_F$ = 20 A, $V_{GS}$ = 0 V | | 1 | 1.5 | V |
| Reverse Recovery Time | $t_{rr}$ | $I_F$ = 20 A, di/dt = 100 A/µs | | 4 | 135 | ns |

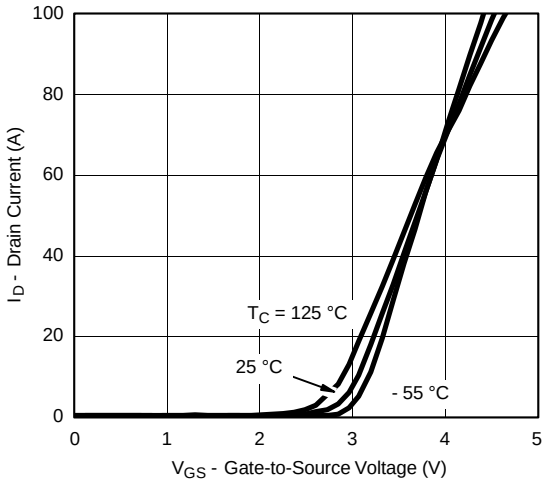
Notes:
a. For design aid only; not subject to production testing.
b. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
c. Independent of operating temperature.

*Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.*

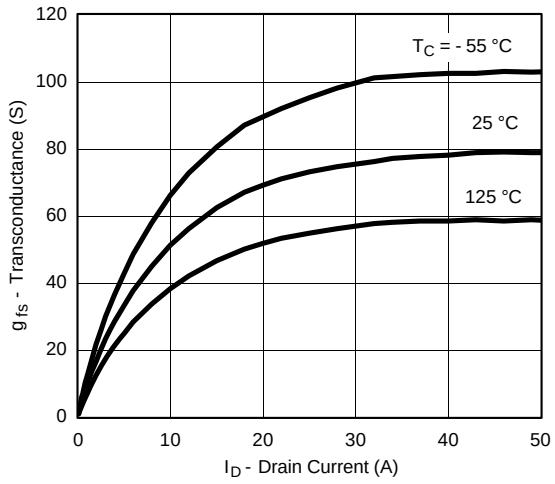
## TYPICAL CHARACTERISTICS (25 °C unless noted)



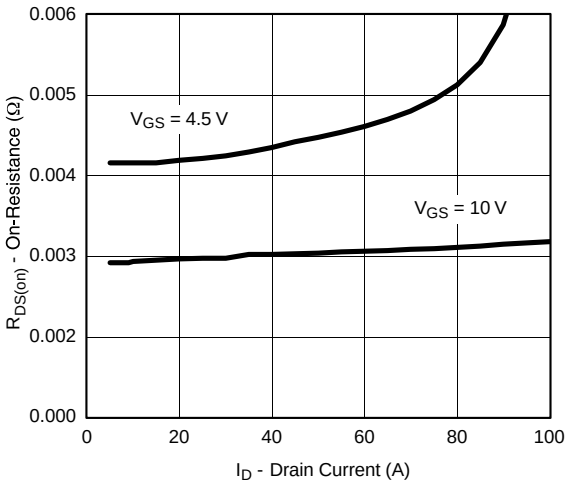

**Output Characteristics**



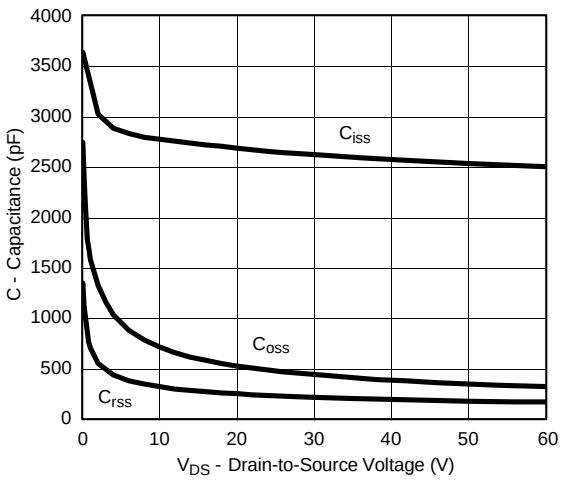

**Transfer Characteristics**



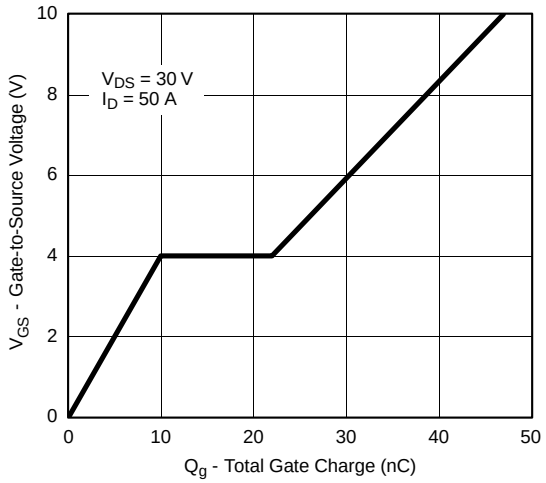

**Transconductance**




**On-Resistance vs. Drain Current**

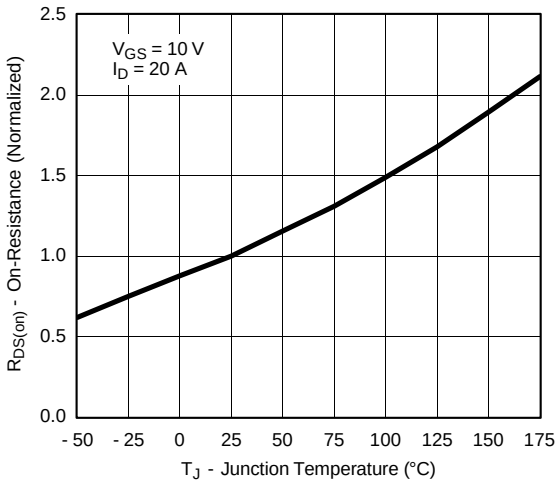



**Capacitance**

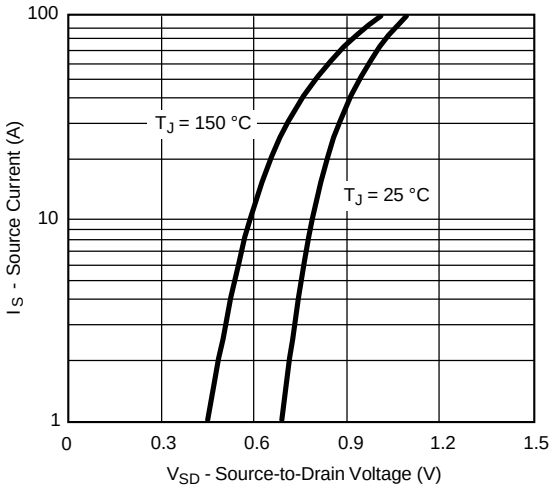



**Gate Charge**

## TYPICAL CHARACTERISTICS (25 °C unless noted)

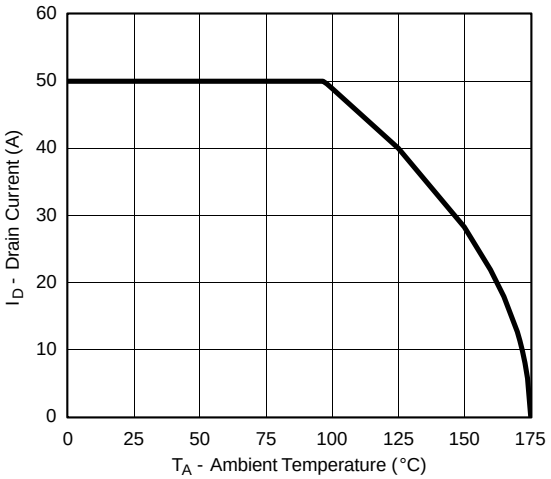



**On-Resistance vs. Junction Temperature**

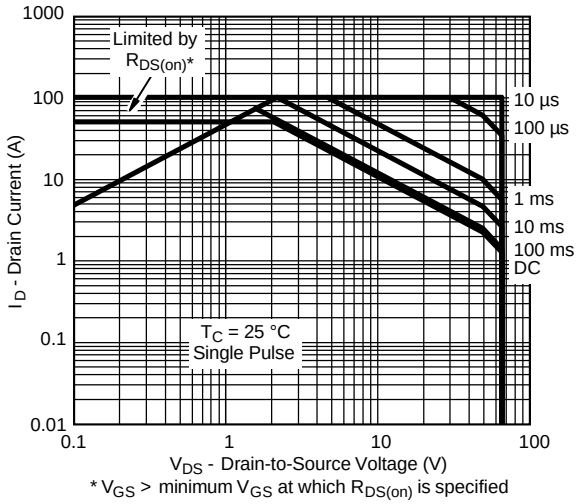



**Source-Drain Diode Forward Voltage**

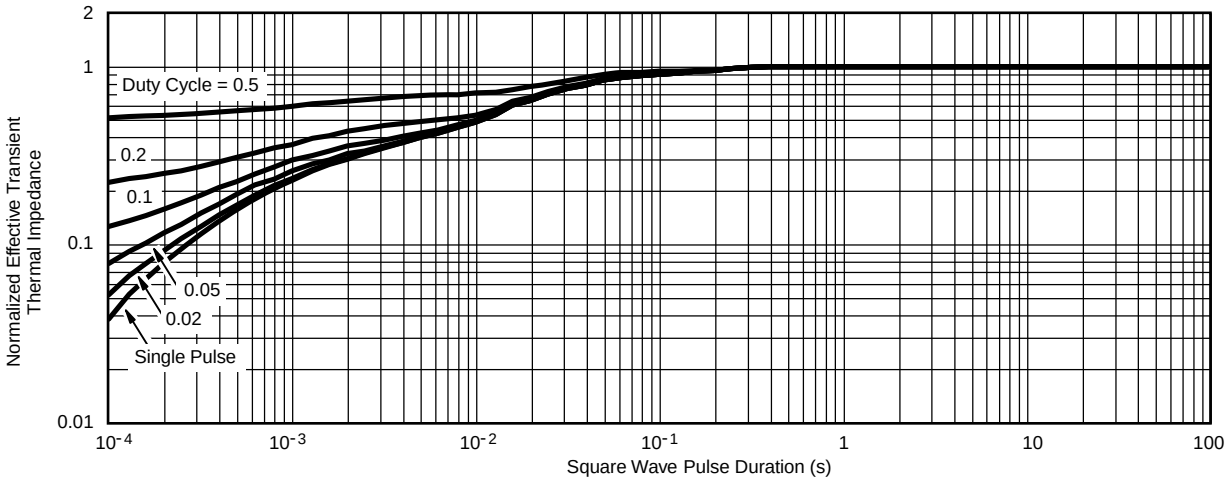
## THERMAL RATINGS




**Maximum Drain Current vs. Ambient Temperature**

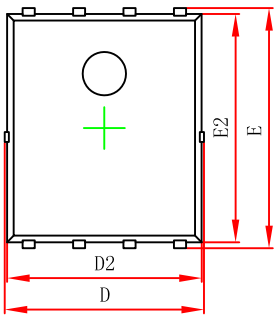



**Safe Operating Area**

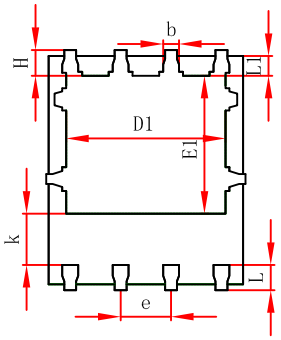



**Normalized Thermal Transient Impedance, Junction-to-Case**

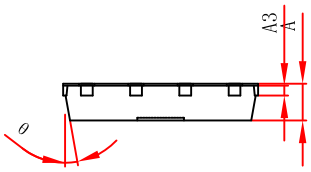
## PDFNWB5x6-8L Package Outline Dimensions




Top View
[顶视图]



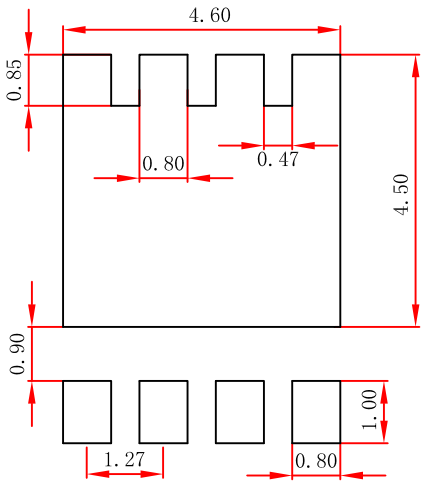

Bottom View
[背视图]




Side View
[侧视图]

| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|---|---|---|---|---|
| | Min. | Max. | Min. | Max. |
| A | 0.900 | 1.000 | 0.035 | 0.039 |
| A3 | 0.254REF. | | 0.010REF. | |
| D | 4.944 | 5.096 | 0.195 | 0.201 |
| E | 5.974 | 6.126 | 0.235 | 0.241 |
| D1 | 3.910 | 4.110 | 0.154 | 0.162 |
| E1 | 3.375 | 3.575 | 0.133 | 0.141 |
| D2 | 4.824 | 4.976 | 0.190 | 0.196 |
| E2 | 5.674 | 5.826 | 0.223 | 0.229 |
| k | 1.190 | 1.390 | 0.047 | 0.055 |
| b | 0.350 | 0.450 | 0.014 | 0.018 |
| e | 1.270TYP. | | 0.050TYP. | |
| L | 0.559 | 0.711 | 0.022 | 0.028 |
| L1 | 0.424 | 0.576 | 0.017 | 0.023 |
| H | 0.574 | 0.726 | 0.023 | 0.029 |
| θ | 10° | 12° | 10° | 12° |

## PDFNWB5x6-8L Suggested Pad Layout




Note:
1.Controlling dimension:in millimeters.
2.General tolerance:±0.05mm.
3.The pad layout is for reference purposes only.

# Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Shenzhen VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Shenzhen VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Shenzhen VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Shenzhen VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental ; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Shenzhen demand that the Shenzhen VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Shenzhen VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Shenzhen VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Shenzhen VBsemi such applications using client did not express their own risk. Contact your authorized Shenzhen VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

# Material Category Policy

**Shenzhen VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be RoHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)**

**Please note that some documents may still refer to Shenzhen VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.**

**Shenzhen VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Shenzhenese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.**