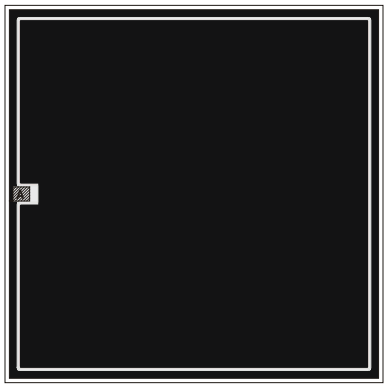
Silicon PIN Photodiode



21887

FEATURES

- Package type: chip
- Package form: single chip
- Dimensions (L x W x H in mm): 2.97 x 2.97 x 0.28
- Radiant sensitive area (in mm^2): 7.7
- High photo sensitivity
- High radiant sensitivity
- Suitable for visible and near infrared radiation
- Fast response times
- Angle of half sensitivity: $\varphi = \pm 60°$
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb-free

e4

RoHS COMPLIANT

HALOGEN FREE GREEN (5-2008)

DESCRIPTION

T1116P is a PIN photodiode with blue enhanced sensitivity and a 7.7 mm^2 sensitive area.

APPLICATIONS

- Blue enhanced photodetectors

GENERAL INFORMATION

The datasheet is based on Vishay optoelectronics sample testing under certain predetermined and assumed conditions, and is provided for illustration purpose only. Customers are encouraged to perform testing in actual proposed packaged and used conditions. Vishay optoelectronics die products are tested using Vishay optoelectronics based quality assurance procedures and are manufactured using Vishay optoelectronics established processes. Estimates such as those described and set forth in this datasheet for semiconductor die will vary depending on a number of packaging, handling, use, and other factors. Therefore sold die may not perform on an equivalent basis to standard package products.

PRODUCT SUMMARY			
COMPONENT	I_{ra} (µA)	φ (deg)	$\lambda_{0.1}$ (nm)
T1116P	43	± 60	350 to 1100

Note

- Test conditions see table “Basic Characteristics”

ORDERING INFORMATION			
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
T1116P-SD-F	Wafer sawn on foil with disco frame	MOQ: 3500 pcs	Chip

Note

- MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V_R	25	V
Junction temperature		T_j	100	°C
Operating temperature range		T_{amb}	-40 to +100	°C
Storage temperature range		T_{stg1}	-40 to +100	°C
Storage temperature range on foil		T_{stg2}	-40 to +50	°C

For technical questions, contact: optochipsupport@vishay.com

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BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)						
PARAMETER	**TEST CONDITION**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Breakdown voltage	I_R = 100 µA, E = 0	$V_{(BR)}$	-	25	-	V
Forward voltage	I_F = 50 mA	V_F	-	1	1.3	V
Reverse dark current	V_R = 10 V, E = 0	$I_{ro}t$	-	2	5	nA
Diode capacitance	V_R = 0 V, f = 1 MHz, E = 0	C_D	-	90	-	pF
	V_R = 3 V, f = 1 MHz, E = 0	C_D	-	30	-	pF
Temperature coefficient of I_K	E_e = 1 mW/cm², λ = 950 nm	TK_{IK}	-	0.1	-	%/K
Reverse light current	E_e = 1 mW/cm², λ = 950 nm, V_R = 5 V	I_{ra}	-	43	-	µA
	E_e = 1 mW/cm², λ = 400 nm, V_R = 5 V	I_{ro}	-	13	-	µA
	E_V = 100 lx, CIE illuminant A, V_R = 5 V	I_{ro}	-	7.1	-	µA
Angle of half sensitivity		φ	-	± 60	-	deg
Wavelength of peak sensitivity		λ_p	-	940	-	nm
Range of spectral bandwidth		$\lambda_{0.1}$	-	350 to 1100	-	nm
Noise equivalent power	V_R = 10 V, λ = 400 nm	NEP	-	1.1×10^{-13}	-	W/√Hz
Rise time	V_R = 5 V, R_L = 500 Ω, λ = 850 nm	t_r	-	40	-	ns
Fall time	V_R = 10 V, R_L = 1 kΩ, λ = 820 nm	t_f	-	40	-	ns

Note

- The measurements are based on samples of die which are mounted on a TO-header without resin coating

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

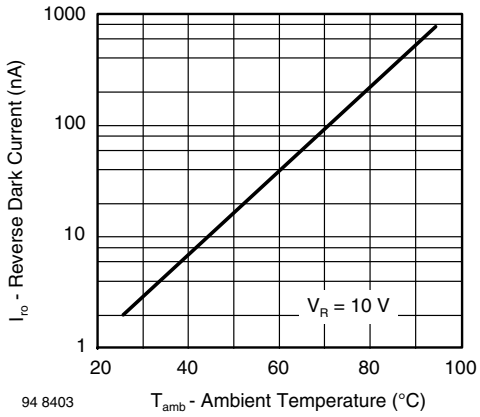


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

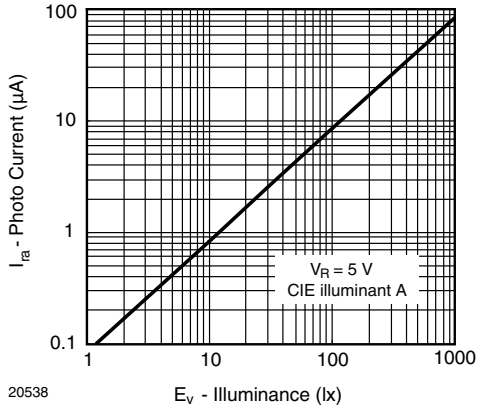


Fig. 3 - Photo Current vs. Illuminance

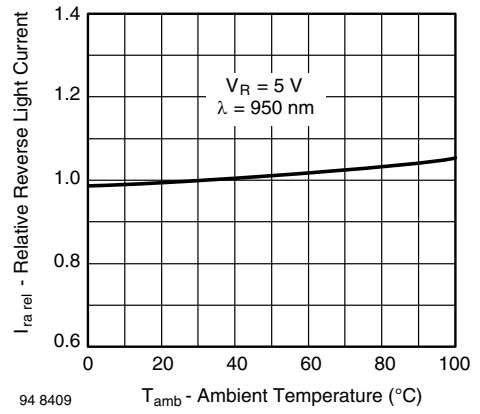


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

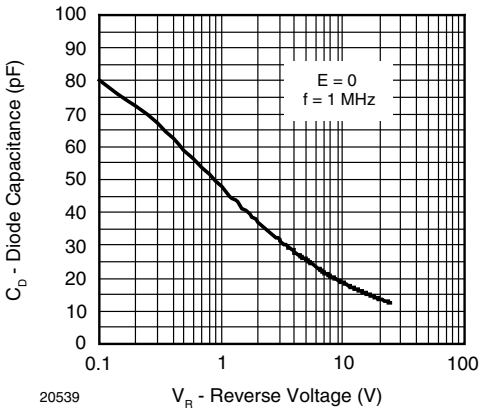


Fig. 4 - Diode Capacitance vs. Reverse Voltage

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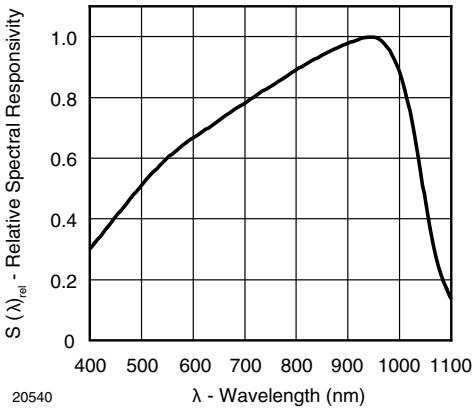


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

MECHANICAL DIMENSIONS					
PARAMETER	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Length of chip edge (x-direction)	L_x	-	2.97	-	mm
Length of chip edge (y-direction)	L_y	-	2.97	-	mm
Sensitive area	A_S		2.77 x 2.77		mm^2
Die height	H	-	0.28	-	mm
Bond pad anode	a x b	-	0.125 x 0.110	-	mm^2

ADDITIONAL INFORMATION	
Frontside metallization, anode	Aluminum
Backside metallization, cathode	NiV-Ag
Dicing	Sawing
Die bonding technology	Epoxy bonding

Note

- All chips are checked in accordance with the Vishay Semiconductor, specification of visual inspection FVOV6870.
  The visual inspection shall be made in accordance with the “specification of visual inspection as referenced”. The visual inspection of chip backside is performed with stereo microscope with incident light and 40x to 80x magnification.
  The quality inspection (final visual inspection) is performed by production. An additional visual inspection step as special release procedure by QM is not installed.

HANDLING AND STORAGE CONDITIONS

- The hermetically sealed shipment lots shall be opened in temperature and moisture controlled cleanroom environment only. It is mandatory to follow the rules for disposition of material that can be hazardous for humans and environment.
- Product must be handled only at ESD safe workstations. Standard ESD precautions and safe work environments are as defined in MIL-HDBK-263.
- Singulated die are not to be handled with tweezers. A vacuum wand with non metallic ESD protected tip should be used.

PACKING

Chips are fixed on adhesive foil. Upon request the foils can be mounted on plastic frame or disco frame. For shipment, the wafers are arranged to stacks and hermetically sealed in plastic bags to ensure protection against environmental influence (humidity and contamination).

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