Discription

The UCLAMP3311PQTCT protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



DFN1006-2L

Features

- ★ Transient protection for high-speed data lines
  IEC 61000-4-2(ESD) ±8kV (Contact)
  ±15kV (Air)
  IEC 61000-4-4(EFT) 40A (5/50 ns)
- ★ Peak power dissipation: 84W (8/20us)
- ★ Working voltages : 3.3V
- ★ Ultra-small package (1.0mmx0.6mmx0.5mm)
- ★ Protects one I/0 line
- ★ Low clamping voltage
- ★ Low leakage current



Circuit Diagram

Ordering Information

Product ID	Pack	Qty(PCS)
UCLAMP3311PQTCT	DFN1006-2L	10000

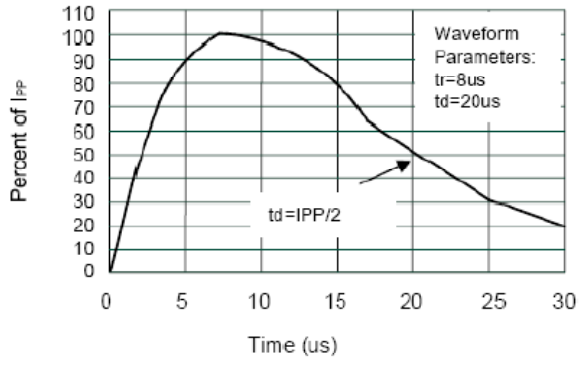
Absolute Ratings(Tamb = 25°C)

Symbol	Parameter	Value	Units
P_{PP}	Peak Pulse Power (t_p = 8/20 μ s)	84	W
T_L	Maximum lead temperature for soldering during 10s	260	°C
T_{stg}	Storage Temperature Range	-55 to +150	°C
T_{op}	Operating Temperature Range	-55 to +150	°C
T_j	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discharge contact discharge	±15 ±8	KV
	IEC61000-4-4 (EFT)	40	A

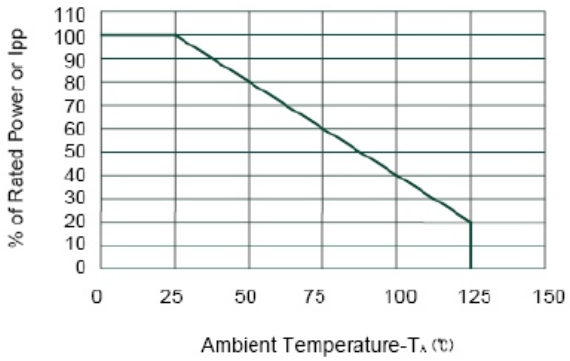
Electrical Characteristics Ratings at 25°C

Symbol	Parameter	Test Condition	Min	Typ	Max	Units
V_{RWM}	Reverse Working Voltage				3.3	V
V_{BR}	Reverse Breakdown Voltage	I_T = 1mA	3.6			V
I_R	Reverse Leakage Current	V_{RWM} = 3.3V			1.0	μA
V_C	Clamping Voltage	I_{PP} = 1A, t_p = 8/20μs			8	V
		I_{PP} = 7A, t_p = 8/20μs			12	V
C_J	Junction Capacitance	V_R = 0V, f = 1MHz		10	15	pF

Typical Characteristics

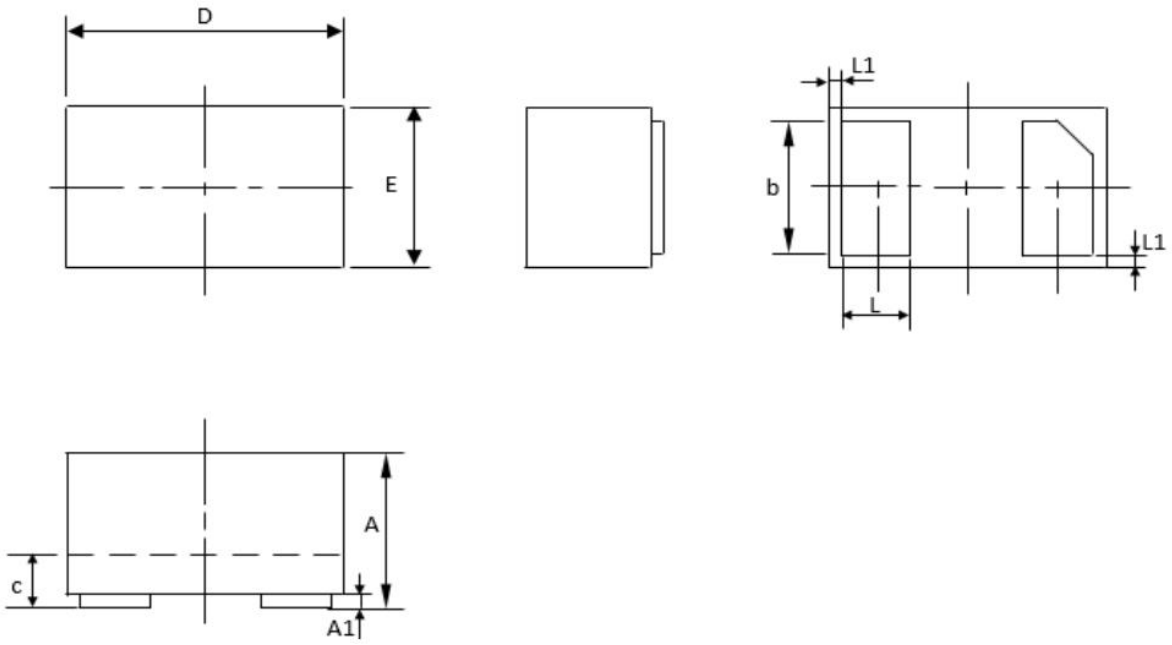


Pulse Waveform



Power Derating Curve

Outline And Dimensions



DFN1006-2L			
Dim	Min	Typ.	Max
A	0.46	0.48	0.50
A1	0	0.02	0.05
b	0.45	0.5	0.55
c	0.1	0.12	0.14
D	0.95	1.00	1.05
E	0.55	0.60	0.65
L	0.20	0.25	0.30
L1	0.035	0.05	0.065
h	0.07	0.12	0.17
All Dimensions in mm			

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