


# PXTA42-Q

## 300 V, 100 mA NPN high-voltage transistor

3 July 2023 **Product data sheet**

## 1. General description

NPN high-voltage transistor in a medium power and flat lead SOT89 (SC-62) Surface-Mounted Device (SMD) plastic package.

PNP complement: PXTA92-Q

## 2. Features and benefits

- High breakdown voltage
- Medium power and flat lead SMD plastic package
- Qualified according to AEC-Q101 and recommended for use in automotive applications

## 3. Applications

- Electronic ballast for fluorescent lighting
- LED driver for LED chain module
- High Intensity Discharge (HID) front lighting
- Automotive motor management
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

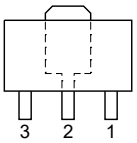
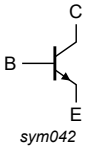
## 4. Quick reference data

**Table 1. Quick reference data**

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $V_{CEO}$ | collector-emitter voltage | open base | | - | - | 300 | V |
| $I_C$ | collector current | | | - | - | 100 | mA |
| $I_{CM}$ | peak collector current | | | - | - | 200 | mA |
| $h_{FE}$ | DC current gain | $V_{CE}$ = 10 V; $I_C$ = 30 mA; $T_{amb}$ = 25 °C | | 40 | - | - | |

## 5. Pinning information

**Table 2. Pinning information**

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|---|---|---|---|---|
| 1 | E | emitter |   SOT89 |   sym042 |
| 2 | C | collector | | |
| 3 | B | base | | |

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# 6. Ordering information

**Table 3. Ordering information**

| Type number | Package | | |
|---|---|---|---|
| | Name | Description | Version |
| PXTA42-Q | SOT89 | plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body | SOT89 |

# 7. Marking

**Table 4. Marking codes**

| Type number | Marking code[1] |
|---|---|
| PXTA42-Q | %1D |

[1] % = placeholder for manufacturing site code

# 8. Limiting values

**Table 5. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|---|---|---|---|---|---|---|
| $V_{CBO}$ | collector-base voltage | open emitter | | - | 300 | V |
| $V_{CEO}$ | collector-emitter voltage | open base | | - | 300 | V |
| $V_{EBO}$ | emitter-base voltage | open collector | | - | 6 | V |
| $I_C$ | collector current | | | - | 100 | mA |
| $I_{CM}$ | peak collector current | | | - | 200 | mA |
| $I_{BM}$ | peak base current | | | - | 100 | mA |
| $P_{tot}$ | total power dissipation | $T_{amb} \leq 25$ °C | [1] | - | 1.3 | W |
| $T_j$ | junction temperature | | | - | 150 | °C |
| $T_{amb}$ | ambient temperature | | | -65 | 150 | °C |
| $T_{stg}$ | storage temperature | | | -65 | 150 | °C |

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 6 $cm^2$.

# 9. Thermal characteristics

**Table 6. Thermal characteristics**

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air | [1] | - | - | 96 | K/W |
| $R_{th(j-sp)}$ | thermal resistance from junction to solder point | | | - | - | 16 | K/W |

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 $cm^2$.


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# 10. Characteristics

**Table 7. Characteristics**

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|---|---|---|---|---|---|---|---|
| $I_{CBO}$ | collector-base cut-off current | $V_{CB}$ = 200 V; $I_E$ = 0 A; $T_{amb}$ = 25 °C | | - | - | 100 | nA |
| $I_{EBO}$ | emitter-base cut-off current | $V_{EB}$ = 6 V; $I_C$ = 0 A; $T_{amb}$ = 25 °C | | - | - | 100 | nA |
| $h_{FE}$ | DC current gain | $V_{CE}$ = 10 V; $I_C$ = 1 mA; $T_{amb}$ = 25 °C | | 25 | - | - | |
| | | $V_{CE}$ = 10 V; $I_C$ = 10 mA; $T_{amb}$ = 25 °C | | 40 | - | - | |
| | | $V_{CE}$ = 10 V; $I_C$ = 30 mA; $T_{amb}$ = 25 °C | | 40 | - | - | |
| $V_{CEsat}$ | collector-emitter saturation voltage | $I_C$ = 20 mA; $I_B$ = 2 mA; $T_{amb}$ = 25 °C | | - | - | 500 | mV |
| $V_{BEsat}$ | base-emitter saturation voltage | | | - | - | 900 | mV |
| $C_{re}$ | feedback capacitance | $V_{CB}$ = 20 V; $I_C$ = 0 A; $i_c$ = 0 A; f = 1 MHz; $T_{amb}$ = 25 °C | | - | - | 3 | pF |
| $f_T$ | transition frequency | $V_{CE}$ = 20 V; $I_C$ = 10 mA; f = 100 MHz; $T_{amb}$ = 25 °C | | 50 | - | - | MHz |

# 11. Test information

## Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

# 12. Package outline

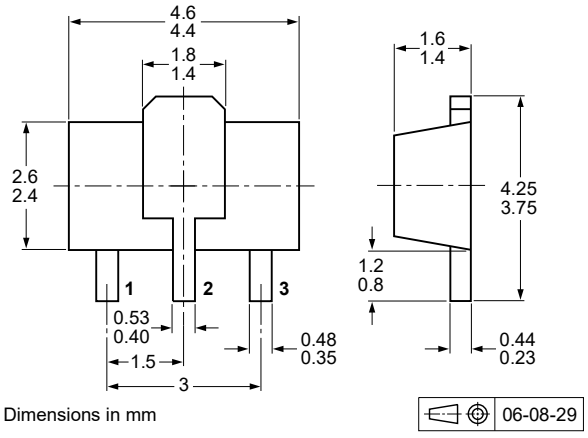



**Fig. 1. Package outline SOT89**


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## 13. Soldering

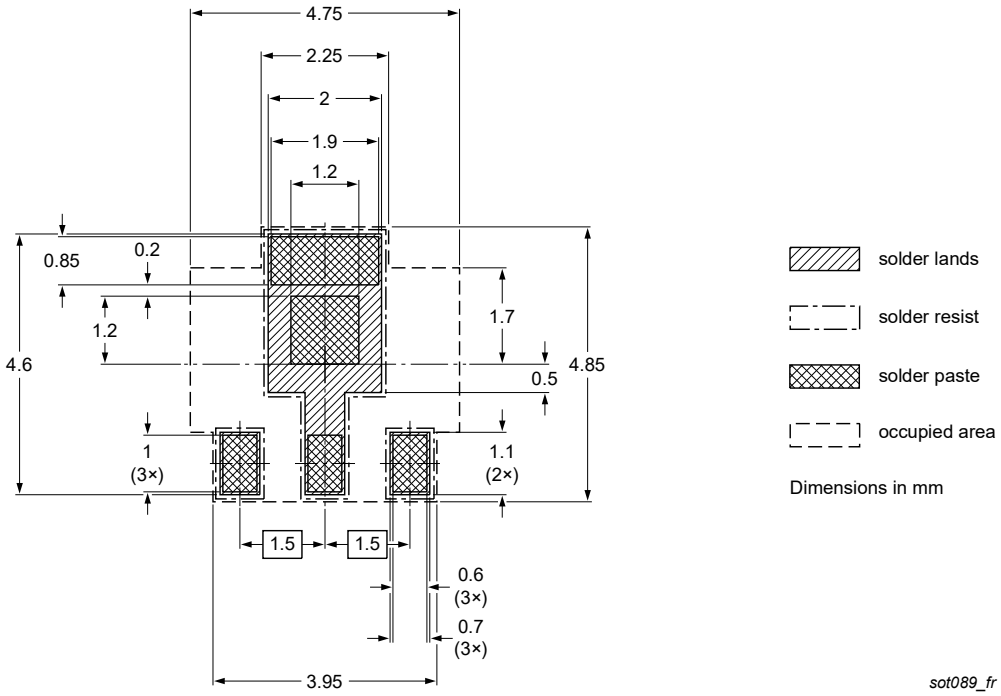



**Fig. 2. Reflow soldering footprint for SOT89**

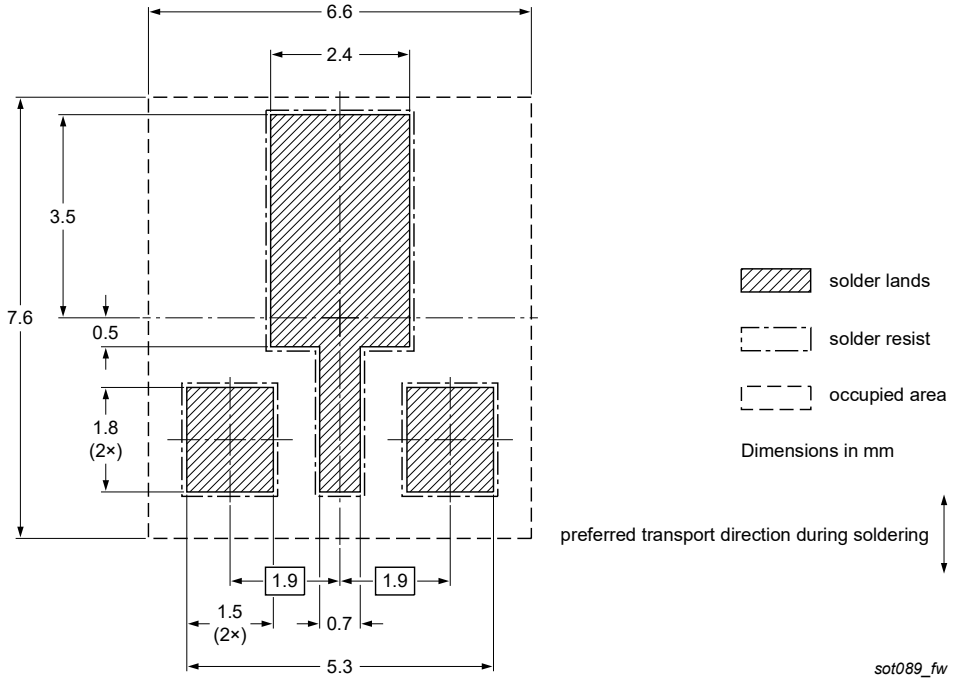



**Fig. 3. Wave soldering footprint for SOT89**


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# 14. Revision history

**Table 8. Revision history**

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|---|---|---|---|---|
| PXTA42-Q v.1 | 20230703 | Product data sheet | - | - |


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## Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|---|---|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
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# Contents

1. General description....1
2. Features and benefits....1
3. Applications....1
4. Quick reference data....1
5. Pinning information....1
6. Ordering information....2
7. Marking....2
8. Limiting values....2
9. Thermal characteristics....2
10. Characteristics....3
11. Test information....3
12. Package outline....3
13. Soldering....4
14. Revision history....5
15. Legal information....6

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